



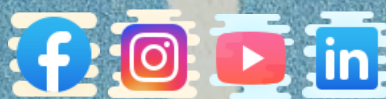
# **Annual Reports of A.Y**

(2018-19, 2019-20, 2020-21, 2021-22)

## **IE(I) ELECTRONICS STUDENTS' CHAPTER**

*Birla Vishvakarma Mahavidyalaya Engineering College*  
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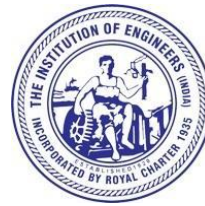
**[ Electronics Department ]**







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**Vallabh-Vidyanagar Program Report**

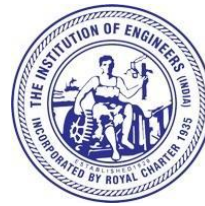


## Content Table

Event Name	Date
Workshop on Editing Tools for Reporting Scientific and Technical Research	18/09/2018
PCB Design Workshop	1/3/2019
Basics of Arudino	1/10/2019
Orientation Program of IE(I)	1/11/2019
Programmable logic control(PLC)	1/19/2019
Basic Robot Building Workshop	2/28/2019
Workshop on Antenna Design using HFSS	6/26/2019
Workshop on MACHINE LEARNING	7/13/2019
Orientation for 1 <sup>st</sup> Year Students of Electronics	7/18/2019
Workshop on NanoTechnology	7/19/2019
Expert talk on Recent Trends in Antenna	7/27/2019
Introduction to Anechoic Chamber	9/4/2019



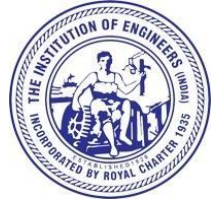
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Event Name	Date
Workshop on “Introduction to MATLAB	9/4/2019
RTOS workshop	9/7/2019
IOT Workshop	9/8/2019
Introduction to PCB Prototype Machine	9/14/2019
Industrial Visit To AIMTRON	9/17/2019
Interaction With M.D. Of Entuple Technologies	10/3/2019
Embedded System Course Project Expo	10/5/2019
Basics of Robotics	10/12/2019
Antenna Measurement Techniques	1/31/2020
Mega Event “ELECTROVATION”	3/5/2020
Webinar on “Living In The 5G World”	5/9/2020
Webinar on “How to convert project into final product”	5/10/2020
Webinar on “Internet of Things”	5/18/2020
Webinar on “Machine Learning”	5/26/2020



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Event Name	Date
Webinar On "Implementation Of Dsp Using Lcdk"	8/18/2020
Webinar On "Industrial Internet Of Things"	9/18/2020
Webinar On Introduction To E-SIM	2/14/2021
Webinar On How To Prepare For Interviews	3/7/2021
Webinar On Connecting Node Mcu with IBM Watson: IOT	3/10/2021
Workshop On Sensors: Second Need Of Electronics	4/3/2021
Webinar On How To Get A Qualitative Internship	5/1/2021
Patents and it's importance for Engineers and Researchers	9/24/2021
Webinar on “Micro to Nano-Electronics”	9/25/2021
Achieve Your GATE Through Dreams	9/4/2021
5 Days demo workshops - "Dream Jobs for IT"	10/4/2021
Expert Lecture on ASIC Verification	10/9/2021
Webinar on “MetaMaterial = Matter + Material”	10/9/2021
Webinar on “Introduction to STM32 and Logic	10/29/2021





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**❖ Workshop on Editing Tools for Reporting Scientific and Technical Research**

**Event name** : Workshop on Editing Tools for Reporting Scientific and Technical Research

**Day & Date** : 18<sup>th</sup> September, 2018

**Time** : 8:00 am to 10:00 am

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. T. D. Pawar, Prof. P. H. Panchal

**Organized for** : 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year

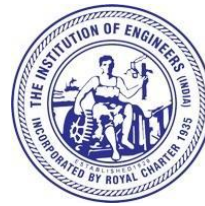


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<b>Specific Subject under which event organized:</b>	Workshop on Editing Tools for Reporting Scientific and Technical Research
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	8:00 am to 10:00 am, 18 <sup>th</sup> September, 2018
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Prof. Nadim Pathan Prof. Chandani
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar Prof. P. H. Panchal
<b>Contact Info. of Coordinators:</b>	9898722601
<b>Total No. of Students</b>	20
<b>Venue Name:</b>	Lab F-105

#### OUTCOMES:

The workshop started at 8:00 am with an Introduction to latex software by experts Prof. Nadim Pathan and Prof. Chandani. Students got the information about the research writing tool and the salient features of the software.

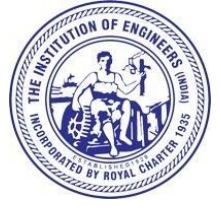




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### PHOTO GALLERY:



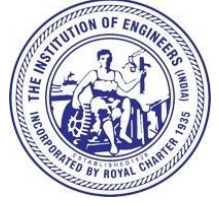
Fig 1: Group Photo



Fig 2: Session delivered by Professors



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**❖ PCB Design Workshop**

**Event name** : PCB Design Workshop

**Day & Date** : 3<sup>rd</sup> January, 2019

**Time** : 8:30 am to 10:15 am

**Venue** : LRUC Hall

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1<sup>st</sup>, 2<sup>nd</sup> and 3<sup>rd</sup> Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Workshop
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



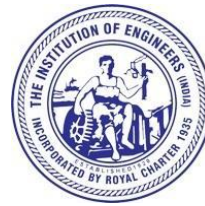


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<b>Date and Time of event:</b>	8:30 am to 10:15 am, 3 <sup>rd</sup> January, 2019
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Arth Shah, Vasishth Vaghela
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. P. H. Panchal
<b>Contact Info. of Coordinators:</b>	9898722601
<b>Total No. of Students</b>	88
<b>Venue Name:</b>	LRUC Hall

#### OUTCOMES:

The workshop started at 8:35 am with an Introduction to “**Basics of PCB Design**” by experts Arth Shah and Vasishth Vaghela. Students were explained the salient features of the development circuit, its origin and how it would be helpful to them in their upcoming future.

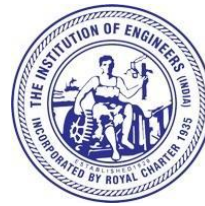
#### PHOTO GALLERY



Fig-1: Group Photo



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**❖ Basics of Arudino**

**Event name** : Basics of Arudino

**Day & Date** : 10<sup>th</sup> January, 2019

**Time** : 8:30 am to 10:15 am

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 2nd, 3rd and 4th Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Microcontroller
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)





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<b>Date and Time of event:</b>	8:30 am to 10:15 am, 10 <sup>th</sup> January, 2019
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Arth Shah, Vasishth Vaghela
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. P. H. Panchal
<b>Contact Info. of Coordinators:</b>	9898722601
<b>Total No. of Students</b>	116
<b>Venue Name:</b>	LRUC Hall

#### OUTCOMES:

The workshop started at 8:35 am with an Introduction to “**Basics of Arduino**” by experts Arth Shah and Vasishth Vaghela. Students were explained the salient features of the development circuit, its origin and how it would be helpful to them in their upcoming future.

#### PHOTO GALLERY



Fig-1: Learning Section

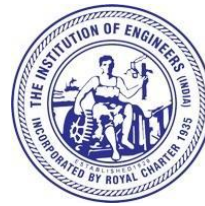


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#### **❖ Orientation Program of IE (I)**

**Event name** : Orientation Program of IE (I)

**Day & Date** : 11th January, 2019

**Time** : 9:00 am to 10:00 am

**Venue** : LRUC Hall

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

#### **Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Orientation Program
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



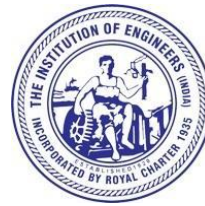


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<b>Date and Time of event:</b>	11 <sup>th</sup> January, 2019 9:00 am to 10:00 am
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. P. H. Panchal
<b>Department Coordinator Name:</b>	Arth Shah, Priti Kumari
<b>Total No. of Students</b>	77
<b>Venue Name:</b>	LRUC Hall

#### OUTCOMES:

The Orientation Program was started at 9:05 am with an Introduction to “**Structure of IE (I)**” by Arth Shah and Priti Kumari. Students were explained what IE (I), What is it works, its origin and how it would be helpful to them in their upcoming future.

#### PHOTO GALLERY:



Fig-1: Orientation Section

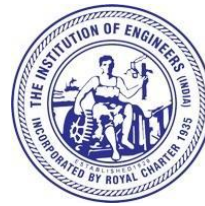


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### **Vallabh-Vidyanagar Program Report**



#### **❖ Programmable logic control (PLC)**

**Event name** : Programmable logic control (PLC)

**Day & Date** : 19th January, 2019

**Time** : 9:30 am to 12:00 pm

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. Kaushika Patel, Prof. Palak Patel

**Organized for** : 1st and 2nd Year B.tech Students

#### **Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> & 2 <sup>nd</sup> year
<b>Specific Subject under which event organized:</b>	Automation and control
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)



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<b>Date and Time of event:</b>	19 <sup>th</sup> January 2019      9:30 am to 12:00 pm
<b>No of Days for event:</b>	01
<b>Event Coordinators:</b>	Dr. Kaushika Patel Prof. Palak Patel
<b>Total No of Students:</b>	40
<b>Venue name:</b>	Embedded system lab (F105)

#### OUTCOMES:

Students were explained the salient features of the development circuit, its origin and how it would be helpful to them in their upcoming future. They were also given a brief introduction to software regarding use of PLC.

The students had in-depth knowledge about the development circuit and its importance. Also, Students were taught the basic programming commands and practice programs.

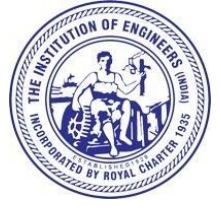




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### PHOTO GALLERY



Fig-1: Introduction Section



Fig-2: Interaction with the participants

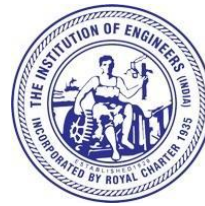


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### **Vallabh-Vidyanagar Program Report**



#### **❖ Basic Robot Building Workshop**

**Event name** : Basic Robot Building Workshop

**Day & Date** : 28<sup>th</sup> February, 2019

**Time** : 8:30 am to 10:15 am

**Venue** : LRUC Hall

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Basic Robot Building Workshop
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



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<b>Date and Time of event:</b>	28 <sup>th</sup> February, 2019 8:30 am to 10:15 am
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Ankur Varma, Arth Shah
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. P. H. Panchal
<b>Contact Info. of Coordinators:</b>	9898722601
<b>Total No. of Students</b>	112
<b>Venue Name:</b>	LRUC Hall

#### OUTCOMES:

The workshop started at 8:35 am with an Introduction to “**Basics of Robotics**” by experts Arth Shah and Ankur Verma. Students were explained the salient features of the development part, its origin and how it would be helpful to them in their upcoming future.

They were also given a brief introduction to a component regarding the use of robots. The students had an in-depth knowledge about the development and its importance. Also, Students were taught the basic commands.



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### PHOTO GALLERY



Fig-1: Introduction section



Fig-2: Learning about basics of Robotics



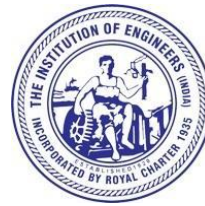


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### **Vallabh-Vidyanagar Program Report**



#### **❖ Workshop on Antenna design using HFSS**

**Event name** : Workshop on Antenna design using HFSS

**Day & Date** : 26<sup>th</sup> June, 2019

**Time** : 10:00 am

**Venue** : F105

**Organized by** : Prof. P. H. Panchal

**Organized for** : 3<sup>rd</sup> Year Students

**Program details** :

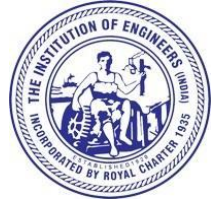
<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	3rd Year
<b>Specific Subject under which event organized:</b>	Antenna Theory
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	10:00 am, 26th June, 2020



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<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Prof. P.H. Panchal
<b>Name of the Coordinator:</b>	Prof. P.H. Panchal
<b>Total No. of Students</b>	30
<b>Venue Name:</b>	F-105

### OUTCOMES:

- Designed small dipole antennas for different frequencies and analyze that and obtained gain plot, S parameter plot, y Parameter plot, Radiation Plot

### PHOTO GALLERY



Fig-1: Expert explaining HFSS Software



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### **Vallabh-Vidyanagar Program Report**



#### **❖ Workshop on MACHINE LEARNING**

**Event name** : Workshop on MACHINE LEARNING

**Day & Date** : 13<sup>th</sup> July 2019

**Time** : 9:00 am to 5:00 pm

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Machine Learning
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	9:30 A.M. 13 <sup>th</sup> July 2019
<b>No of Days for event:</b>	01

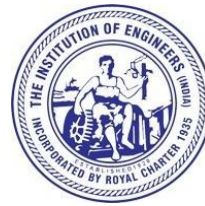


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



<b>Name of speakers:</b>	Dr. Kiran Trivedi
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal
<b>Total No of Students:</b>	40
<b>Venue name:</b>	Embedded system lab (F105)

### OUTCOMES:

Workshop on “**Introduction to Machine Learning**”, began with a technical talk by Student Expert Preeti Kumari (B.tech 4<sup>th</sup> year) and explanation of python and its various libraries. The library for discussion today was ‘Pandas’. The session was very interactive. They provided a hands-on session to all students. Practical based learning approach gave an effective outcome. The participants were very enthusiastic and actively performed everything.

Students were taught how to use its IDE tool for programming and also taught the steps for how to install the IDE in the Windows environment.

### PHOTO GALLERY



Fig 1: Poster of Event





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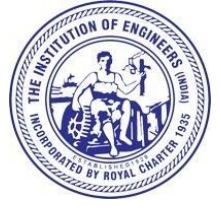


Fig 2: welcome of expert by Prof. Parul Panchal



Fig-1: Group Photo with Speaker

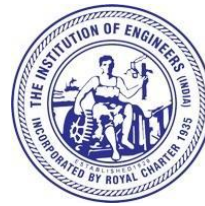


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### Vallabh-Vidyanagar Program Report



#### ❖ Orientation for 1<sup>st</sup> Year Students of Electronics

**Event name** : Orientation for 1<sup>st</sup> Year Students of Electronics  
Department

**Day & Date** : 18<sup>th</sup> July, 2019

**Time** : 3:00 pm

**Venue** : F-105 Lab

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : First Year Students

#### Program details :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	First Year Students
<b>Specific Subject under which event organized:</b>	Introduction to IEI Electronics BVM



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	18 <sup>th</sup> July, 2019, 3:00 pm
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Dr. T.D. Pawar, Dr. J. M. Rathod, Prof. Parul Panchal, Students. Preeti Kumari and Kirti Tandel
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No. of Students</b>	90
<b>Venue Name:</b>	F-105 Lab

#### OUTCOMES:

- How the IEI Electronics BVM chapter contributes to the development of the students' profile.
- The newly coming first year students get to know about the history and the work of the IEI (Electronics) .
- They get the introduction by the Department Coordinator, chairperson and the secretary of the IEI BVM Electronics chapter.

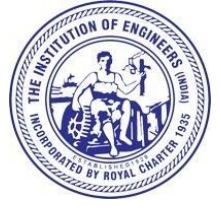




# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### PHOTO GALLERY:



Fig-1: Brief introduction of the Event



Fig-2: First Year Students



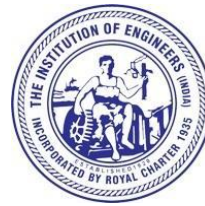


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

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### **Vallabh-Vidyanagar Program Report**



#### **❖ Workshop on NanoTechnology**

**Event name** : Workshop on NanoTechnology

**Day & Date** : 19<sup>th</sup> July, 2019

**Time** : 10:45 am to 12:00 pm

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 2nd, 3rd and 4th Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	2 <sup>nd</sup> , 3 <sup>rd</sup> & 4 <sup>th</sup> year
<b>Specific Subject under which event organized:</b>	VLSI
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	19 <sup>th</sup> July 2019 10:45 am

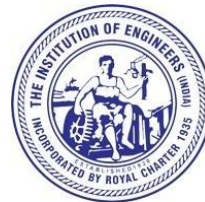


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>No of Days for event:</b>	01
<b>Name of speakers:</b>	Dr. Rutu Parekh
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	30
<b>Venue name:</b>	Embedded system lab (F105)

#### OUTCOMES:

The workshop started at 10:45 am with an Introduction to **NanoTechnology** by the expert. Students were explained the basis of nanotechnology and how it would be helpful to them in their upcoming future. They were also given a brief introduction to nanoelectronics.

Students have gained general knowledge of nanotechnology, their future scopes and Nano technology as an emerging technology.

The participants were very enthusiastic and actively performed everything.

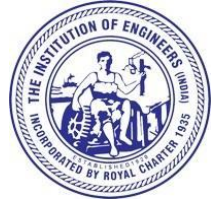


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



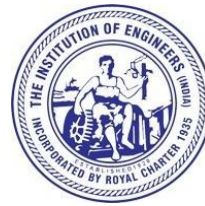
Fig-1: Group Photo with Speaker



Fig-2: Learning Section



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**Vallabh-Vidyanagar Program Report**



**❖ Expert talk on Recent Trends in Antenna**

**Event name** : Expert talk on Recent Trends in Antenna  
**Day & Date** : 27<sup>th</sup> July, 2020  
**Time** : 9:00 am to 10:30 am  
**Venue** : LRUC  
**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal  
**Organized for** : 1st Year B.tech Students

**Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1st year
<b>Specific Subject under which event organized:</b>	Antenna
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)

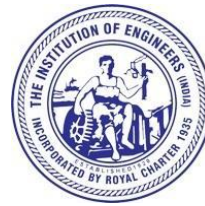




# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

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<b>Date and Time of event:</b>	27th July 2020 9:00 am to 10:30 am
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Er. Himanshu Ankleshvariya
<b>Department Coordinator Name:</b>	Prof. J.M.Rathod, Prof.Parul Panchal
<b>Total No. of Students</b>	70
<b>Venue Name:</b>	LRUC

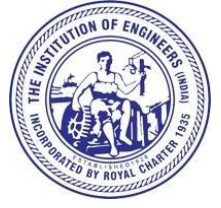
### **OUTCOMES :**

IE (I) Electronics Students Chapters has organized an expert talk on Recent Trend in Antenna on 27 July 2020. 70 participants present have participated for this expert talk.

It was organized for 1st year students. Er. Himanshu Ankleshvariya from System Level Solutions Private Limited was the expert of this talk. He had given the idea about research in antennas.



# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig-1: Pre-Event click of Experts and Professors



Fig-1: In-event click of the expert and students



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**Vallabh-Vidyanagar Program Report**



**❖ Introduction to Anechoic Chamber**

**Event name** : Introduction to Anechoic Chamber  
**Day & Date** : 04<sup>th</sup> September, 2019  
**Time** : 10:00 am  
**Venue** : ELARC Lab  
**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal  
**Organized for** : Final Year Students  
**Program details** :

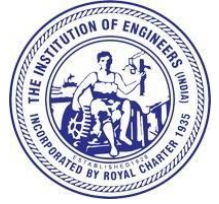
<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	Final Year Students
<b>Specific Subject under which event organized:</b>	Antenna
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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Vallabh-Vidyanagar Program Report



<b>Date and Time of event:</b>	04 <sup>th</sup> September, 2019, 10:00 am
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Er. Umesh paldhikar Er. Gaurav Jindal
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No. of Students</b>	50
<b>Venue Name:</b>	ELARC Lab

### OUTCOMES:

- Able to measure various parameters of the antenna using the anechoic chamber.
- Able to design/simulate Microstrip antenna using HFSS software.

### PHOTO GALLERY



Fig-1: Glimpse of Introduction of Anechoic Chamber



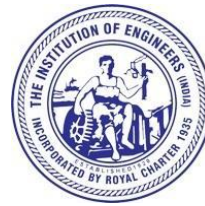


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

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### **Vallabh-Vidyanagar Program Report**



#### **❖ Workshop on “Introduction to MATLAB”**

**Event name** : Workshop on “Introduction to MATLAB”

**Day & Date** : 04th & 05th September, 2019

**Time** : 9:00 am to 10:30 am

**Venue** : Microprocessor Lab(F104)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

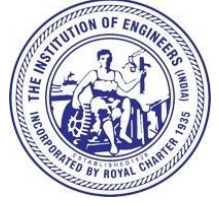
**Organized for** : 1st and 2nd Year B.tech Students

#### **Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> & 2 <sup>nd</sup> year
<b>Specific Subject under which event organized:</b>	All subjects of Electronics
<b>Professional body under which event organized:</b>	IE(I) Electronics Student’s branch (Gujarat Section)



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<b>Date and Time of event:</b>	04th and 05th September, 2019 9:00 am to 10:30 am
<b>No. of days for event:</b>	02
<b>Name of Speakers:</b>	Yukti Patel, Vrushal Vara
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No. of Students</b>	40
<b>Venue Name:</b>	Microprocessor Lab(F104)

**OUTCOMES:**

Students get familiar with the MATLAB environment. They also learned how to use MATLAB for mathematical calculations and for graphical interpretation of any signal or a system.



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**❖ RTOS workshop**

**Event name** : RTOS workshop  
**Day & Date** : 7<sup>th</sup> September, 2019  
**Time** : 09:30 am  
**Venue** : F104  
**Organized by** : Prof. Anita Bhatt  
**Organized for** : 3<sup>rd</sup> Year Students

**Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	Embedded & IOT System Design
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



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<b>Date and Time of event:</b>	09:30 A.M., 7 <sup>th</sup> September, 2019
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Mr. Hiren Soni
<b>Department Coordinator Name:</b>	Prof. Anita Bhatt
<b>Total No. of Students</b>	50+
<b>Venue Name:</b>	F104

#### OUTCOMES:

- Program the ARM7 processor for many applications and projects.

#### PHOTO GALLERY



Fig-1: Faculty and Expert Group photo





# Birla Vishvakarma Mahavidyalaya

## Engineering College

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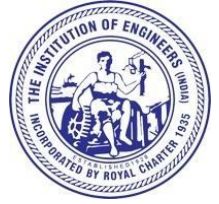


Fig-2: Event photo



Fig 3: Certificate Distribution



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**❖ IOT Workshop**

**Event name** : IOT Workshop

**Day & Date** : 8<sup>th</sup> September, 2019

**Time** : 11:00 am

**Venue** : LRUC Hall

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 3<sup>rd</sup> Year Student

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	3 <sup>rd</sup> Year
<b>Specific Subject under which event organized:</b>	IOT
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Date and Time of event:</b>	8 <sup>th</sup> September, 2019 10:00 am
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Krupal ka. Patel
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No. of Students</b>	100
<b>Venue Name:</b>	LRUC Hall

#### OUTCOMES:

- Work on projects based on IOT using MQTT protocol, Raspberry Pi & various types of sensors.

#### PHOTO GALLERY



Fig-1: Inauguration Section





# Birla Vishvakarma Mahavidyalaya

## Engineering College

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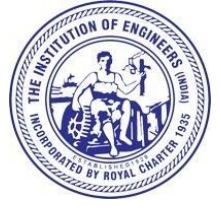


Fig-2: Workshop Glimpse



Fig 3: Group Photo



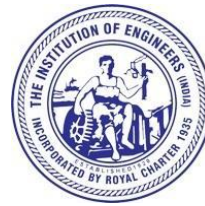


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

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### **Vallabh-Vidyanagar Program Report**



#### **❖ Introduction to PCB Prototype Machine**

**Event name** : Introduction to PCB Prototype Machine

**Day & Date** : 14<sup>th</sup> September, 2019

**Time** : 10:00 am to 11:30 am

**Venue** : F104

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1<sup>st</sup> Year Students

**Program details** :

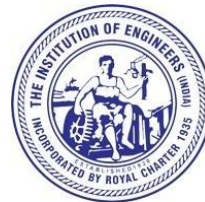
<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> Year
<b>Specific Subject under which event organized:</b>	Workshop
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	14 <sup>th</sup> September, 2019 10:00 am
<b>No. of days for event:</b>	01



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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Vallabh-Vidyanagar Program Report



<b>Name of Speakers:</b>	Engineers from Royal sales and corporation
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No. of Students</b>	50+
<b>Venue Name:</b>	F104

### OUTCOMES:

- Fabricate dipole, rectangular patch, circular patch, triangle patch, split ring patch antenna using the PCB prototype machine.
- Design two-layer PCB using machines.

### PHOTO GALLERY



Fig-1: Demo session of PCB prototype machine



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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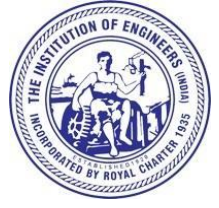


Fig-2: Group Photo



Fig 3: Expert explaining about PCB Prototype Machine

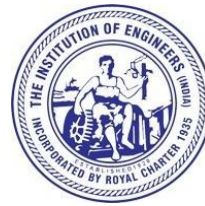


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



#### ❖ Industrial Visit to AIMTRON

**Event name** : Industrial visit to AIMTRON

**Day & Date** : 17<sup>th</sup> September, 2019

**Time** : 10:00 am to 8:00 pm

**Venue** : AIMTRON Company

**Organized by** : Prof. P. H. Panchal, Prof. M. M. Khambalkar

**Organized for** : 1<sup>st</sup> Year Students

#### Program details :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	UG
<b>Specific Subject under which event organized:</b>	Workshop
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)



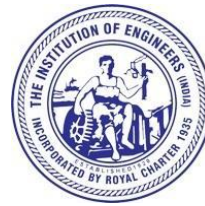


# **Birla Vishvakarma Mahavidyalaya**

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### **Vallabh-Vidyanagar Program Report**



<b>Date and Time of event:</b>	17 <sup>th</sup> September, 2019 10:00 am
<b>No of Days for event:</b>	01
<b>Department Coordinator Name:</b>	Prof. Parul H. Panchal, Prof. M. M. Khambalkar
<b>Industry Name:</b>	Aimtron Industry, Waghodiya, Vadodara.
<b>Total No of Students:</b>	76
<b>Venue name:</b>	F-104

#### **OUTCOMES:**

All the students saw the machines as well as their workings. The guide gave guidance about safety measures, he also explained the working of machines and their various parts. “Each machine has its own uniqueness” was the lesson the students learned from it.

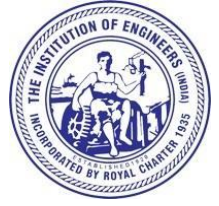
The most important part that all liked about Aimtron was it is an international full service company that provides cost effective, integrated and high quality solutions.



# Birla Vishvakarma Mahavidyalaya

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### PHOTO GALLERY



Fig 1: Group photo



Fig 2: Group Photo



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**❖ Interaction with MD of ENTUPLE**

**Event name** : Interaction with MD of ENTUPLE

**Day & Date** : 3th October, 2019

**Time** : 10:00 am onwards

**Venue** : F-104

**Organized by** : Dr. J. M. Rathod, Prof. Parul Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details :**

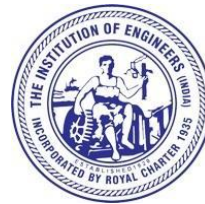
<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	UG
<b>Specific Subject under which event organized:</b>	Antenna
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	3th October, 2019 11 am
<b>No of Days for event:</b>	01



# Birla Vishvakarma Mahavidyalaya

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Vallabh-Vidyanagar Program Report



<b>Name of speakers:</b>	MD of Entuple Mr. Mehta
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	36
<b>Venue name:</b>	F-104

### OUTCOMES:

The interaction included a questioner by Mr. Mehta, in which the students presented their career plans. Mr. Mehta then provided an insight on the opportunities and alternative career options in various sectors for Electronics and EC engineers.

### PHOTO GALLERY

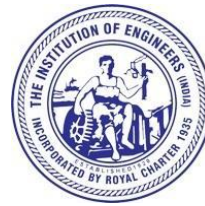


Fig-1: Event click of Experts Students and Professors





**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
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**Vallabh-Vidyanagar Program Report**



**❖ Embedded System Course Project Expo**

**Event name** : Embedded System Course Project Expo

**Day & Date** : 5<sup>th</sup> October, 2019

**Time** : 10:00 am onwards

**Venue** : F-104

**Organized by** : Dr. D. M. Patel and Prof. Anita Bhatt.

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	UG
<b>Specific Subject under which event organized:</b>	Embedded systems course project Expo
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)

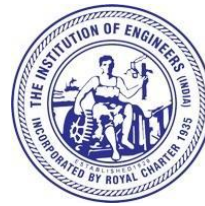


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Date and Time of event:</b>	5th October, 2019 , 10 am
<b>No of Days for event:</b>	01
<b>Name of speakers:</b>	Mr. Hiren Soni
<b>Department Coordinator Name:</b>	Dr. D. M. Patel Prof. Anita Bhatt
<b>Total No of Students:</b>	76
<b>Venue name:</b>	F-104

#### OUTCOMES:

Students were instructed how to make different kinds of projects inspired by the latest embedded projects.

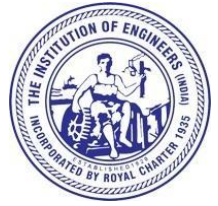
They have presented various projects on FPGA, VLSI, Image processing, Robotics There was even a competition amongst students for their projects



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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Vallabh-Vidyanagar Program Report



### PHOTO GALLERY



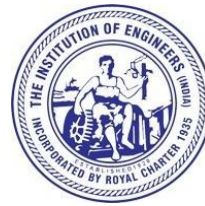
Fig 1: Poster of Event



Fig-2: Experts and Professors



**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
**Managed by CVM**  
**Vallabh-Vidyanagar Program Report**



**❖ Basics of Robotics**

**Event name** : Basics of Robotics

**Day & Date** : 12<sup>th</sup>, 13<sup>th</sup> October, 2019 (2 Days Event)

**Time** : 9:30 am to 5:00 pm

**Venue** : Embedded system lab (F105)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st and 2nd Year B.tech Students

**Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> year
<b>Specific Subject under which event organized:</b>	Microcontroller, Robotics
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)



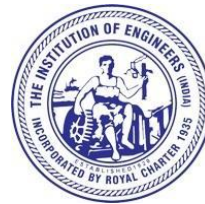


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

**Managed by CVM**

### **Vallabh-Vidyanagar Program Report**



<b>Date and Time of event:</b>	12 <sup>th</sup> , 13 <sup>th</sup> October, 2019, 9:30am to 5 pm
<b>No of Days for event:</b>	02
<b>Name of speakers:</b>	Er. Pritesh kukadiya
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No of Students:</b>	40
<b>Venue name:</b>	Embedded system lab (F105)

#### **OUTCOMES:**

The session was conducted by Er. Pritesh Kukadia(Field application engineer at SLS Pvt. Ltd.) and Truth Pandya(Embedded system developer at Gipitronix Pvt. Ltd.). The main agenda of this workshop was to enhance the knowledge of students in the area of Robotics.

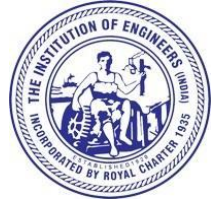
After this 2 day's workshop, 1<sup>st</sup> your students learned about Arduino basics, Arduino programming, coding, attachments of various parts like motor and wheels, different circuits, Wire management etc.



# Birla Vishvakarma Mahavidyalaya

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Vallabh-Vidyanagar Program Report



### PHOTO GALLERY



Fig-1: Poster of the Event

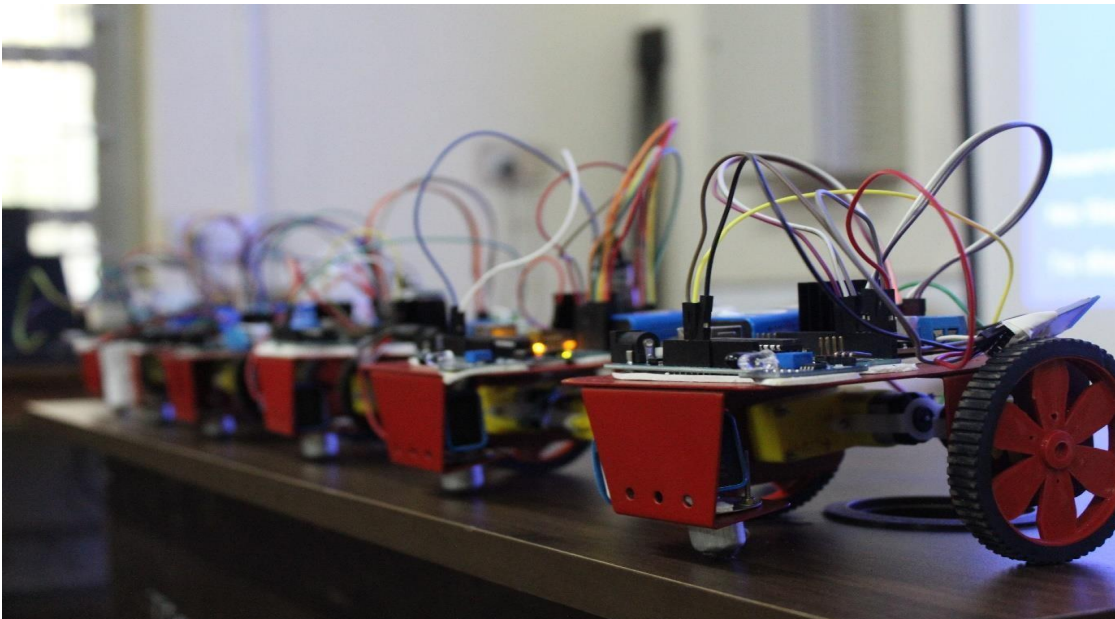


Fig-2: Picture of Robots Made by Students



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



#### ❖ Antenna Measurement Techniques

**Event name** : Antenna Measurement Techniques

**Day & Date** : 31st January & 1st February, 2020

**Time** : 10:00 am onward (31st January/1st February)

**Venue** : ELARC Lab, LRUC Hall

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : Young Researchers

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	Young researchers
<b>Specific Subject under which event organized:</b>	Antenna Measurement Techniques-2020
<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)

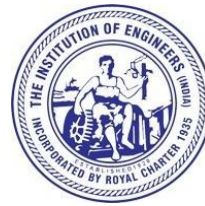


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Date and Time of event:</b>	31st January & 1st February, 2020, 10:00 am
<b>No. of days for event:</b>	02
<b>Name of Speakers:</b>	Dr. Shobhit Patel, Dr. Ganga Prasad Mr. Gaurav Jindal, Prof. Shahid Modasiya
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No. of Students</b>	50+
<b>Venue Name:</b>	ELARC Lab, LRUC Hall

#### OUTCOMES:

- Expose real time antenna measurements using anechoic chamber and microwave analyzer.
- Acquire knowledge for measuring antenna parameters like Gain, return loss and radiation pattern that helps research developers.
- Design the antenna using various software like HFSS, CST Studio etc.
- Analyze the various high frequency antennas.
- Measure various antenna parameters using a vector network analyzer.





# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report



## PHOTO GALLERY

The poster is for a workshop titled 'AMT-2020' (Antenna Measurement Techniques) held from 31st January to 1st February, 2020. It is organized by the Electronics Engineering Department of BVM Engineering College. The poster features logos of the Birla Vishvakarma Mahavidyalaya Engineering College, NPIU, and The Institution of Engineers (India). It also mentions sponsorship and approval by TEQIP-III & IE (I) V V Nagar Local Centre. A photograph of a breadboard circuit and an antenna symbol are included.

चारुतर विद्यामंडल - वल्लभ विद्यानगर  
कर्मण्येवाधिकारस्ते

**NPIU**

WORK IS WORSHIP

THE INSTITUTION OF ENGINEERS (INDIA)  
INCORPORATED BY ROYAL CHARTER 1935

**Birla Vishvakarma Mahavidyalaya Engineering College**  
(An Autonomous Institution)  
Managed by Charutar Vidya Mandal

**TEQIP-III & IE (I) V V Nagar Local Centre Sponsored & approved**  
**Two Day's National Workshop**  
On  
**ANTENNA MEASUREMENT TECHNIQUES**

**AMT-2020**  
31<sup>st</sup> January  
to 1<sup>st</sup> February, 2020

**Organised By**  
**Electronics Engineering Department, BVM Engineering College**

Fig-1: Poster of the Event



# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report

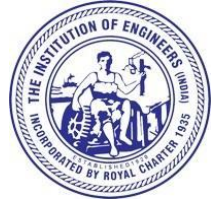


Fig-2: Inauguration Ceremony



Fig-3: Group Photo





# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report

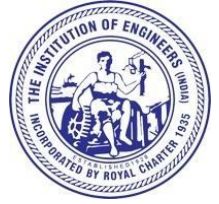


Fig-4: Dr. J. M. Rathod delivering a session on research scope



Fig-5: Dr. Shobhit Patel delivering a session on Graphene Patch Antenna



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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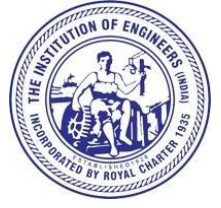


Fig-6: Dr. Ganga Prasad Pandey delivering a session



Fig-7: Mr. Gaural Jindal delivering hands-on Session at ELARC CoE





# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report

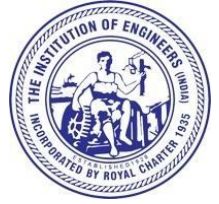


Fig-8: Dr. Y. P. Kosta delivering the Session



Fig-9: Dr. Y. P. Kosta delivering the Session



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## Engineering College

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Vallabh-Vidyanagar Program Report

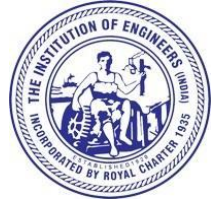


Fig-10: Dr. Trushit Upadhyaya delivering Session



Fig-11: Prof. Shahid Modasiya delivering Session





# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report

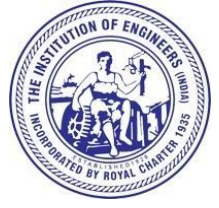


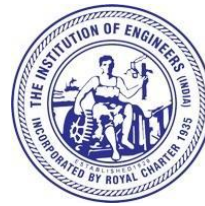
Fig-12: Mr. Abhishek Dholakiya delivering Session



Fig-13: Valedictory Session



**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
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**Vallabh-Vidyanagar Program Report**



**❖ MEGA EVENT “ELECTROVATION”**

**Event name** : MEGA EVENT “ELECTROVATION”

**Day & Date** : 5<sup>th</sup> March, 2020 & 6<sup>th</sup> March, 2020

**Time** : 10:00 am onwards

**Venue** : Electronics Department

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	All level of Electronics Students
<b>Specific Subject under which event organized:</b>	All Electronics Subject
<b>Event Organized:</b>	Code Sense PCB Piquancy Architect the Antennae Circuit Breaker
<b>Workshop Organized:</b>	Image Processing on Pi



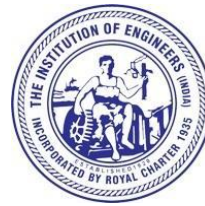


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	5 <sup>th</sup> March & 6 <sup>th</sup> March, 2020, 10:00 am
<b>No. of days for event:</b>	02
<b>Name of Speakers:</b>	Er. Bhaskar Trivedi, Er. Vishal Kharadi
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No. of Students</b>	110+
<b>Venue Name:</b>	ELARC Lab, LRUC Hall

#### OUTCOMES:

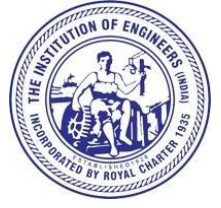
IE (I) Students' team is declared for the new academic year 2019-20. Students IEI team has organized four competition events and a workshop on image processing. They organized competitions based on the electronics subjects. Students learn a lot from the various events organized under tag Electrovation under IE (I) students' chapter.



# Birla Vishvakarma Mahavidyalaya

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### PHOTO GALLERY:



Fig 1:Certificate Distribution



Fig 5: Group photo with Experts



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**Vallabh-Vidyanagar Program Report**



❖ **Webinar on “Living In The 5G World”**

**Event name** : Webinar on “Living In The 5G World”  
**Day & Date** : 9<sup>th</sup> May, 2020  
**Time** : 2:00 pm to 5:00 pm  
**Venue** : Virtual Session on Google meet  
**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal  
**Organized for** : 1st, 2nd and 3rd Year B.tech Students  
**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1st, 2nd, 3rd & 4th year
<b>A professional body under which webinar organized:</b>	IE(I) Electronics Students' Chapter BVM (Gujarat Section)
<b>Date and Time of the webinar:</b>	10th May, 2020 2:00 pm to 5:00 pm



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>No of Days for the webinar:</b>	01
<b>Expert Details</b>	Mr. Subhas Mondal
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No of Students registered:</b>	550+
<b>YouTube channel :</b>	<a href="https://www.youtube.com/watch?v=EfFSf8xOpWs">https://www.youtube.com/watch?v=EfFSf8xOpWs</a> <a href="https://www.youtube.com/watch?v=eD51-u0_u2g">https://www.youtube.com/watch?v=eD51-u0_u2g</a>

#### OUTCOMES:

The entire session was a hands-on session in which experts had done a micro project on Machine Learning from scratch to 550+ participants, 300 were the concurrent viewers on YouTube. 2000+ viewers benefit from the video available on our YouTube channel. Including all the webinars we have gained 100+ subscribers

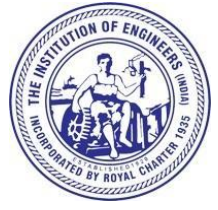
An E-certificate was given to all the participants who had attended the session.

<https://www.youtube.com/watch?v=EfFSf8xOpWs>  
[https://www.youtube.com/watch?v=eD51-u0\\_u2g](https://www.youtube.com/watch?v=eD51-u0_u2g)





# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig-1: Poster of the Event

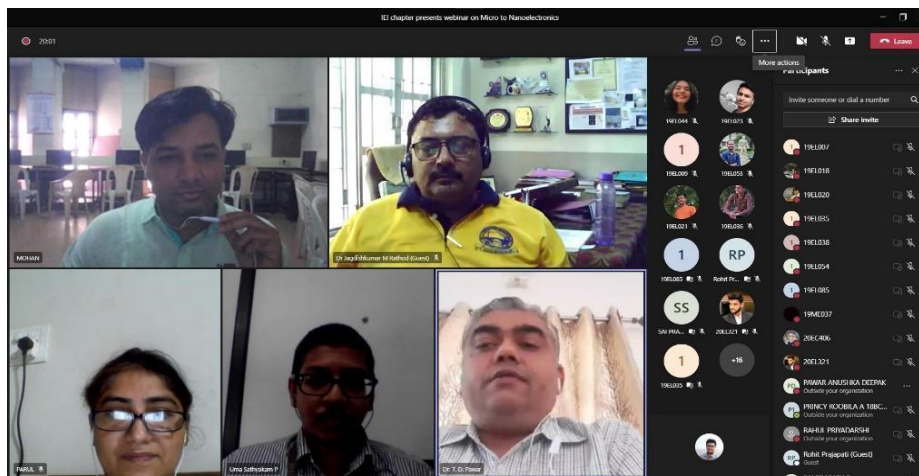
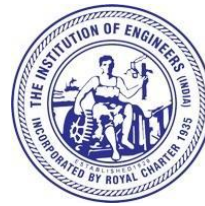


Fig-2: Inaugural session



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**Vallabh-Vidyanagar Program Report**



**❖ Webinar on “How to convert project into final product”**

**Event name** : Webinar on “How to convert project into final product”

**Day & Date** : 10<sup>th</sup> May, 2020

**Time** : 2:00 pm to 5:00 pm

**Venue** : Virtual Session on Google meet

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1st, 2nd, 3rd & 4th year
<b>A professional body under which webinar organized:</b>	IE(I) Electronics Students' Chapter BVM (Gujarat Section)
<b>Date and Time of the webinar:</b>	10th May, 2020 2:00 pm to 5:00 pm



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>No of Days for the webinar:</b>	01
<b>Expert Details</b>	Mr. Pritesh Kukadia
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H Panchal
<b>Total No of Students registered:</b>	550+
<b>YouTube channel :</b>	<a href="https://www.youtube.com/watch?v=EfFSf8xOpWs">https://www.youtube.com/watch?v=EfFSf8xOpWs</a> <a href="https://www.youtube.com/watch?v=eD51-u0_u2g">https://www.youtube.com/watch?v=eD51-u0_u2g</a>

#### OUTCOMES:

The entire session was a hands-on session in which experts had done a micro project on Machine Learning from scratch to 550+ participants, 300 were the concurrent viewers on YouTube. 2000+ viewers benefit from the video available on our YouTube channel. Including all the webinars we have gained 100+ subscribers

An E-certificate was given to all the participants who had attended the session.

<https://www.youtube.com/watch?v=EfFSf8xOpWs>  
[https://www.youtube.com/watch?v=eD51-u0\\_u2g](https://www.youtube.com/watch?v=eD51-u0_u2g)



# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig-1: Poster of the Event

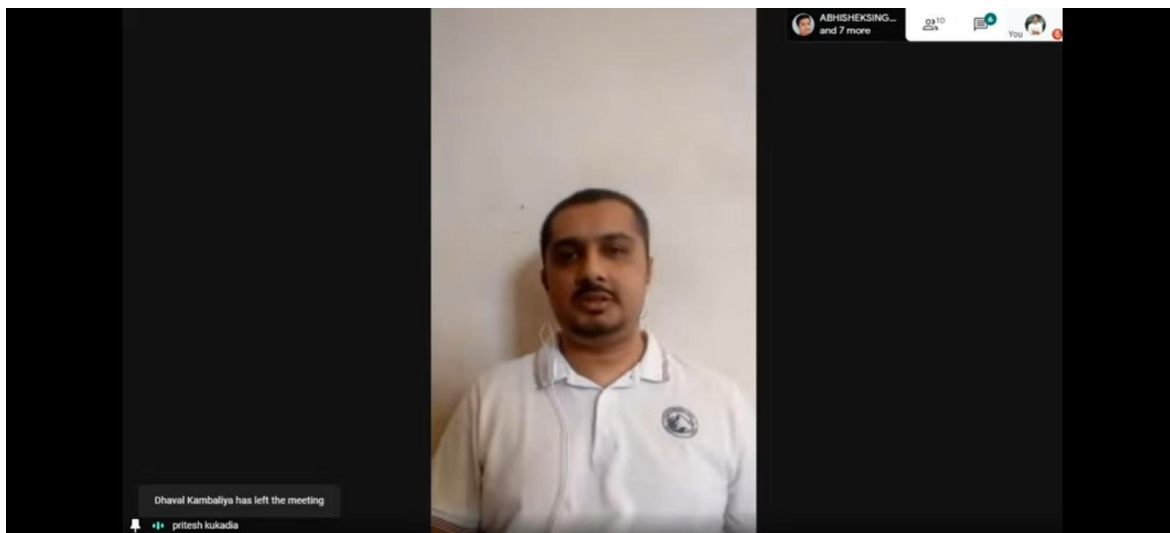


Fig-2: Expert delivering session



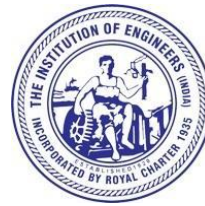


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



#### ❖ Webinar on “Internet of Things”

**Event name** : Webinar on “Internet of Things”

**Day & Date** : 18<sup>th</sup> May, 2020

**Time** : 11:00 am to 1:00 pm

**Venue** : Youtube live

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : B.tech Students

#### Program details :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anan
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1st, 2nd, 3rd & 4th year
<b>A professional body under which webinar organized:</b>	IE(I) Electronics Students' Chapter BVM (Gujarat Section)
<b>Date and Time of the webinar:</b>	18th May, 2020 11:00 am to 1:00 pm



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Expert Details</b>	Mr. Sanjay Singh
<b>No of Days for the webinar:</b>	01
<b>Total No of Students registered:</b>	1200+
<b>YouTube channel :</b>	<a href="https://www.youtube.com/watch?v=KqhOqtiUX6c">https://www.youtube.com/watch?v=KqhOqtiUX6c</a>

### OUTCOMES:

The entire session was a hands-on session in which experts had done a micro project on Machine Learning from scratch to 1200+ participants, 650 were the concurrent viewers on YouTube. 5300+ viewers benefit from the video available on our YouTube channel. Including all the webinars we have gained 100+ subscribers

### PHOTO GALLERY



Fig-1: Poster of Event



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**Vallabh-Vidyanagar Program Report**



**❖ Webinar on “Machine Learning”**

**Event name** : Webinar on “Machine Learning”

**Day & Date** : 26th May, 2020

**Time** : 11:00 am to 1:00 pm

**Venue** : Youtube live

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> , 3 <sup>rd</sup> & 4 <sup>th</sup> year
<b>A professional body under which webinar organized:</b>	IE(I) Electronics Students’ Chapter BVM (Gujarat Section)
<b>Date and Time of the webinar:</b>	26 <sup>th</sup> May, 2020 11:00 am to 1:00 pm
<b>No of Days for the webinar:</b>	01



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



<b>Expert Details</b>	Mr. Rahul Pathak
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students registered:</b>	1600+
<b>YouTube channel :</b>	<a href="https://www.youtube.com/channel/UCPGrwR4__qvSkE2QgAvnW-w">https://www.youtube.com/channel/UCPGrwR4__qvSkE2QgAvnW-w</a>

#### OUTCOMES:

The entire session was a hands-on session in which experts had done a micro project on Machine Learning from scratch to 1600+ participants, 1000 were the concurrent viewers on YouTube. 7500+ viewers benefit from the video available on our YouTube channel.

Including all the four webinars we have gained 850+ subscribers (nearly 550 subscribers increased in this webinar). The total views on our channel are more than 13,000 with more than 1, 25,000 watched minutes

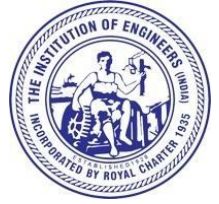




# Birla Vishvakarma Mahavidyalaya

## Engineering College

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Vallabh-Vidyanagar Program Report



### PHOTO GALLERY

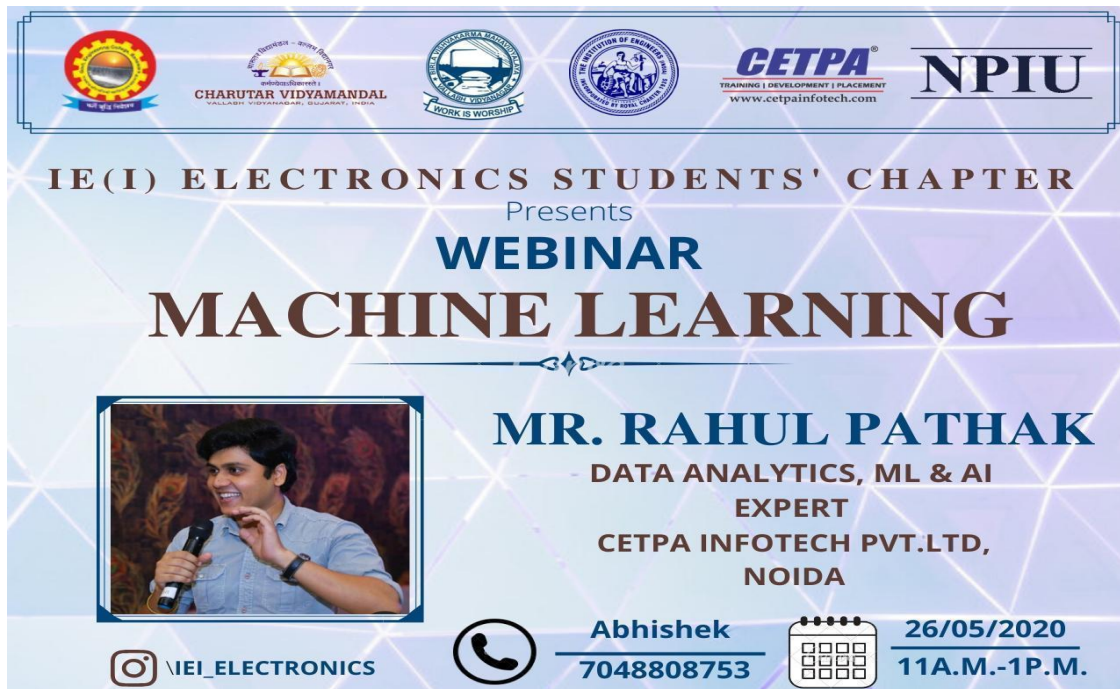


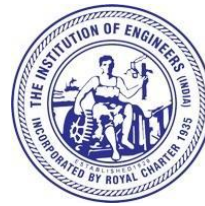
Fig-1: Poster of the Event



Fig 2: Principal Sir address the student



**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
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**Vallabh-Vidyanagar Program Report**



**❖ Webinar on Implementation of DSP using LCDK**

**Event name** : Webinar on Implementation of DSP using LCDK

**Day & Date** : 18<sup>th</sup> August, 2020

**Time** : 8:30 am to 10:00 am

**Venue** : Google Meet

**Organized by** : Dr. M. S. Holia

**Organized for** : 2nd, 3rd and 4th Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	2 <sup>nd</sup> , 3 <sup>rd</sup> & 4 <sup>th</sup> year
<b>Specific Subject under which event organized:</b>	Digital Signal Processing
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	18 <sup>th</sup> August, 2020 8:30 am to 10:00 am
<b>No of Days for event:</b>	01



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### Vallabh-Vidyanagar Program Report



<b>Name of speakers:</b>	Bharat R.
<b>Department Coordinator Name:</b>	Dr. Mehfuza S. Holia
<b>Total No of Students:</b>	40
<b>Venue name:</b>	Google Meet Online

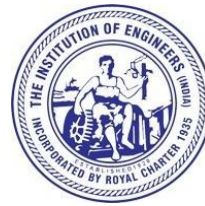
#### OUTCOMES:

The webinar was started at 6:30 pm with an Introduction to **DIGITAL SIGNAL PROCESSING** by the expert Bharat sir. Students were explained the basis of digital signal processing and how it would be helpful to them in their upcoming future. They were also given a brief introduction to digital signal processing.

Students have gained general knowledge of implementation process of digital signal in LCDK, and learn above given concepts



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**❖ Webinar on “Industrial Internet of Things”**

**Event name** : Webinar on “Industrial Internet of Things”

**Day & Date** : 18<sup>th</sup> September, 2020

**Time** : 7:00 pm to 9:00 pm

**Venue** : Youtube live

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> , 3 <sup>rd</sup> & 4 <sup>th</sup> year
<b>A professional body under which webinar organized:</b>	IE(I) Electronics Students’ Chapter BVM (Gujarat Section)
<b>Date and Time of the webinar:</b>	18 <sup>th</sup> September, 2020 7:00pm to 9:00pm



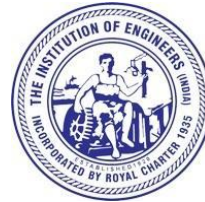


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



<b>No of Days for the webinar:</b>	01
<b>Expert Details:</b>	Madhu Kant Patel
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students registered:</b>	300+
<b>YouTube channel :</b>	<a href="https://www.youtube.com/channel/UCPGwR4__qvSkE2QgAvnW-w">https://www.youtube.com/channel/UCPGwR4__qvSkE2QgAvnW-w</a>

#### OUTCOMES:

“WEBINAR ON INDUSTRIAL INTERNET OF THINGS” was started with the warm welcome of experts and participants. He has completed his graduation from ISRO. Experts covered the topics like the basic knowledge about IOTs and IITO, Industry 4.0, IOT AI solution and different uses of IOT like home automation, RTO, public transport etc.

Participants were also free to ask the question to experts and experts covered each and every question and explained in depth.

The last expert also explains the conclusion of the session about what we covered in the session so participants cannot forget the main topics of the session. And there were 300+ viewers in the session.

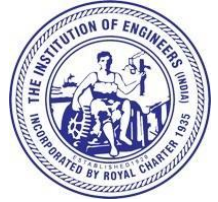


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



## PHOTO GALLE

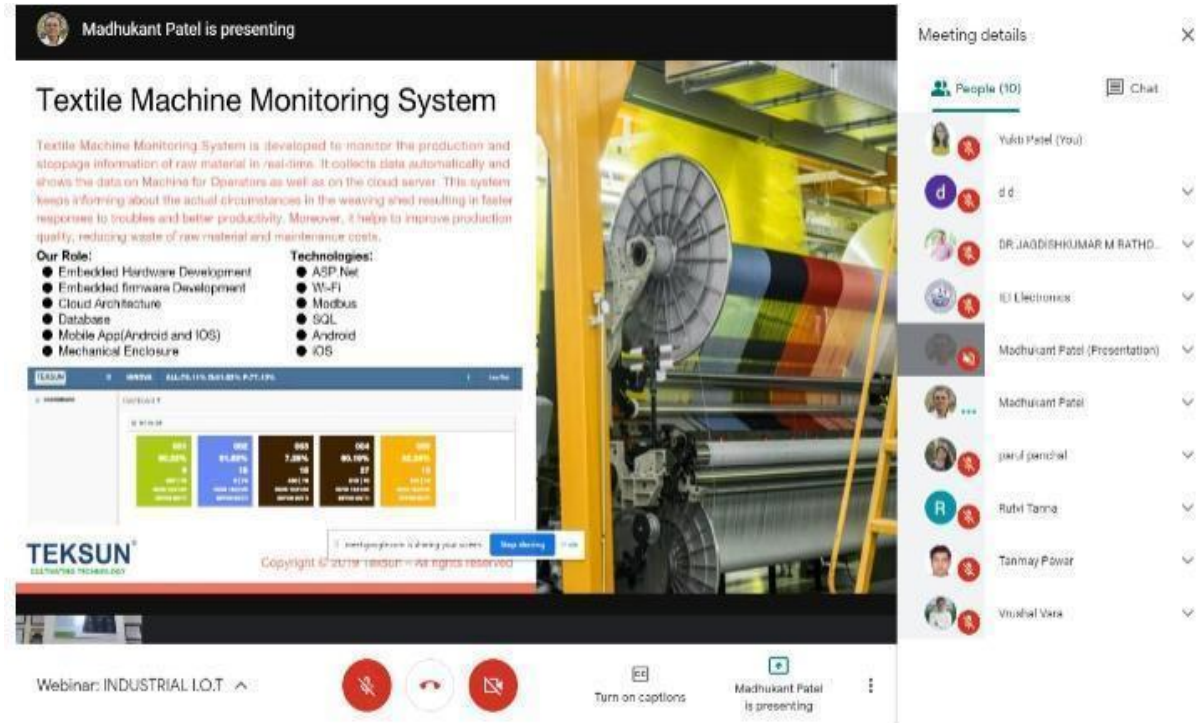
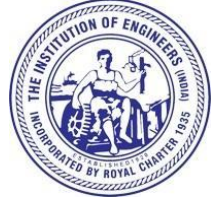


Fig 1: Webinar Screenshot



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❖ **Webinar on “Introduction to E-sim”**

**Event name** : Webinar on “Introduction to E-sim”  
**Day & Date** : 14<sup>th</sup> February, 2021  
**Time** : 10:00 am to 1:00 pm  
**Venue** : Youtube live  
**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal  
**Organized for** : B.tech Students  
**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	1 <sup>st</sup> year
<b>Specific Subject under which event organized:</b>	Introduction of Software
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)



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<b>Date and Time of event:</b>	14 <sup>th</sup> February, 2021 10:00 am to 1:00 pm
<b>No of Days for event:</b>	01
<b>Name of speakers:</b>	Mr. Dhruvan Dangar
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	40
<b>Venue name:</b>	Online platform ( Google Meet )

#### OUTCOMES:

Students were taught how to use eSim software and tools for designing, Simulation and testing. The students were also taught about how they can install software and design actual printed circuit boards. The students were taught about various fundamental concepts and scenarios of real life working problems.



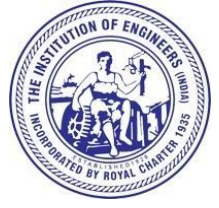


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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## PHOTO GALLERY

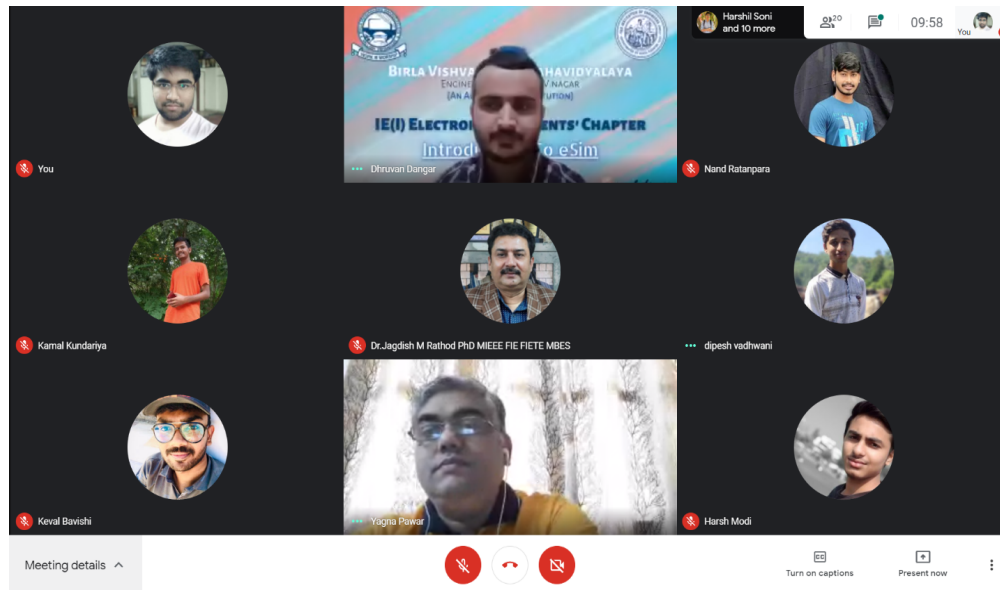


Fig 1: introduction of IE (I)

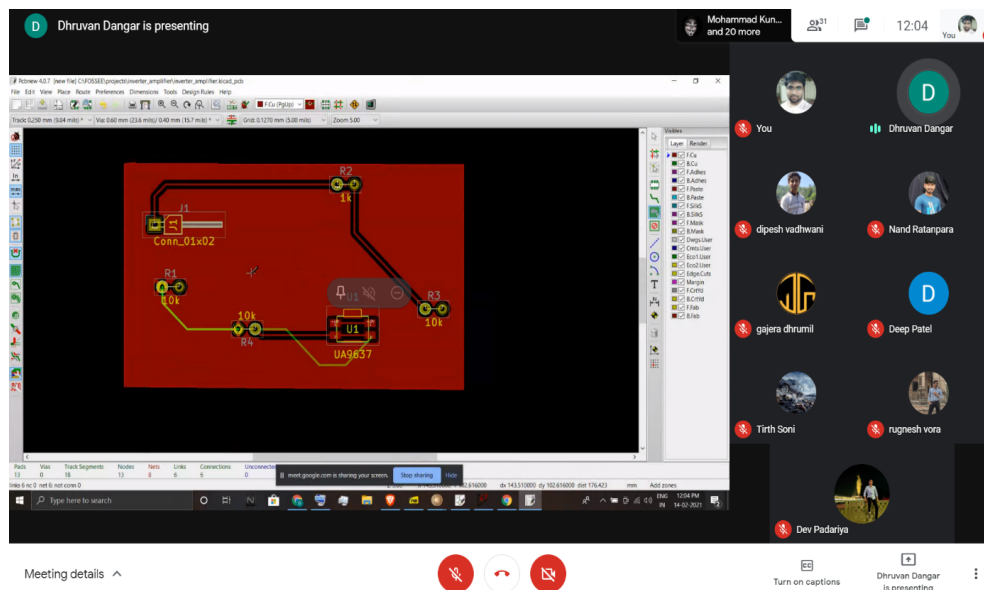
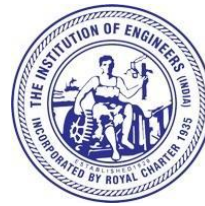


Fig 2: PCB created on E - Sim



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**❖ Webinar on: How to prepare for interview**

**Event name** : Webinar on: How to prepare for interview

**Day & Date** : 7<sup>th</sup> March, 2021

**Time** : 10:00 am onwards

**Venue** : Virtual Session on Google meet

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	UG
<b>Specific Subject under which event organized:</b>	Communication Skill
<b>Professional body under which event organized:</b>	IE (I) electronics student branch (Gujarat Section)



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## **Engineering College**

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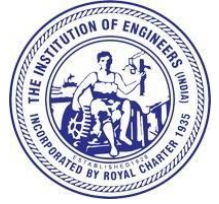
<b>Date and Time of event:</b>	7 <sup>th</sup> March, 2021 10 am
<b>No of Days for event:</b>	01
<b>Name of speakers:</b>	Ms. Nikita Rana
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	110
<b>Venue name:</b>	Virtual Session on Google meet

### **OUTCOMES:**

Students were instructed how to attempt a professional interview and how to deal with interviewers. She also elaborated the ideas about gestures, postures and facial expressions. Nikita ma'am said that **"THE ART OF COMMUNICATION IS THE LANGUAGE OF LEADERSHIP"**.



# Birla Vishvakarma Mahavidyalaya Engineering College Managed by CVM Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig-1: Poster of the Event

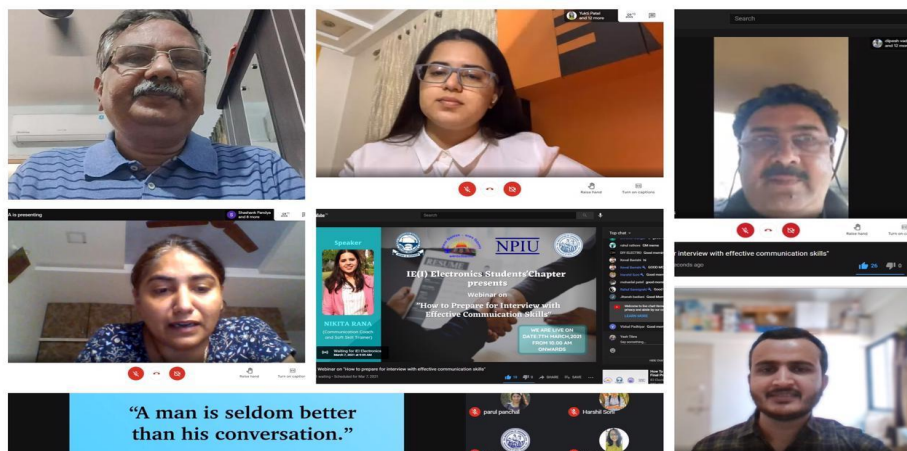


Fig 2: Screen Shot of the Webinar





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**❖ WEBINAR ON CONNECTING NODE MCU WITH IBM  
WESTON: IOT**

**Event name** : Webinar on Connecting node mcu with IBM weston IOT

**Day & Date** : 10<sup>th</sup> March, 2021

**Time** : 10:00 am onwards

**Venue** : Virtual Session on Google meet

**Organized by** : Prof. Anita Bhatt

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	UG
<b>Specific Subject under which event organized:</b>	IoT
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	10 <sup>th</sup> March, 2021 10 am Onward

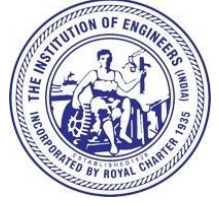


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



<b>No of Days for event:</b>	01
<b>Name of speakers:</b>	Prachi Jain & Ruchi Patel
<b>Department Coordinator Name:</b>	Prof. Anita Bhatt
<b>Total No of Students:</b>	110
<b>Venue name:</b>	Virtual Session on Google meet

#### OUTCOMES:

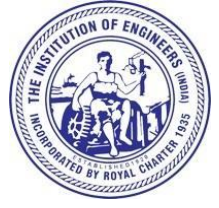
Cloud computing is considered as one of most accomplished and ubiquitous paradigms in the 21st century, especially for the service related- computing. It has been credited for the revolution it has brought by abstracting the complex and expansive computing infrastructure underneath.



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### PHOTO GALLERY:

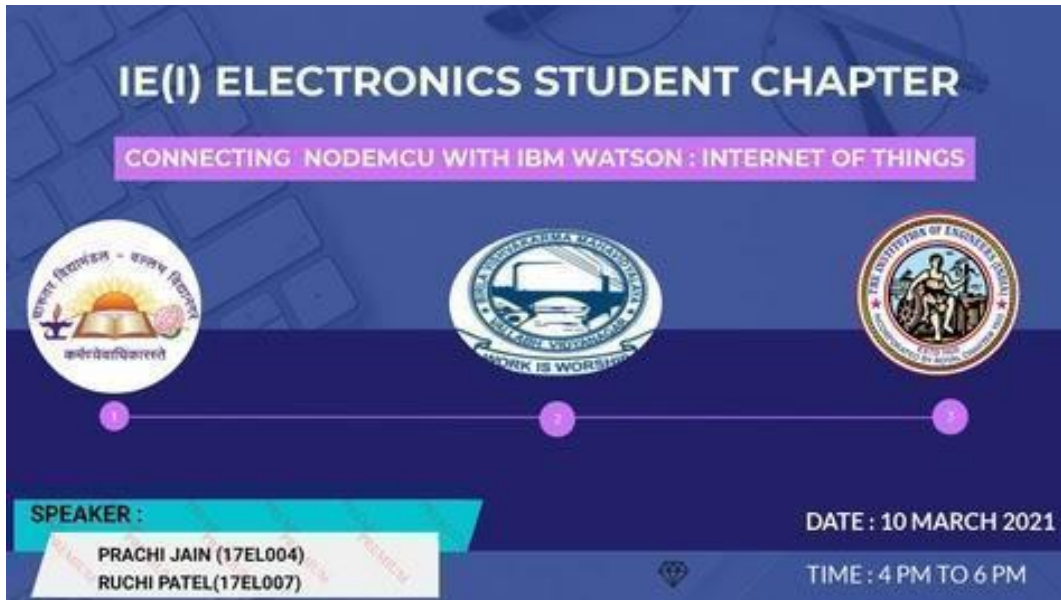


Fig 1: Poster of Event



Fig 2: Screen Shot of the Webinar

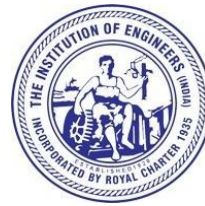


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### Vallabh-Vidyanagar Program Report



#### ❖ Sensors: Second need of electronics

**Event name** : Sensors: Second need of electronics

**Day & Date** : 3<sup>rd</sup> & 4<sup>th</sup> April 2021

**Time** : 10:00 am to 12:30 pm (April 3),  
02:00 pm to 04:30 pm (April 4)

**Venue** : Virtual Session on Google Meet (Online)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1st, 2nd and 3rd Year B.tech Students

#### Program details :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	Sensors



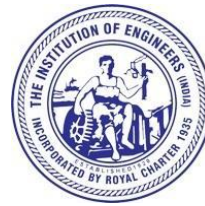


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### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	10:00 am 3 <sup>rd</sup> April 2021 02:00 pm 4 <sup>th</sup> April 2021
<b>No of Days for event:</b>	02
<b>Name of speaker:</b>	Mr. Pritesh Kukadia
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	55
<b>Venue name:</b>	Virtual Session on YouTube Live

#### OUTCOMES:

Students were given knowledge about the sensors and transducers, its types, characteristics and different types of sensors and its applications. Also the students were given a perspective about the future trends in sensor technology

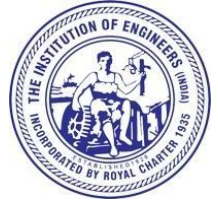


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### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig-1: Poster of Event

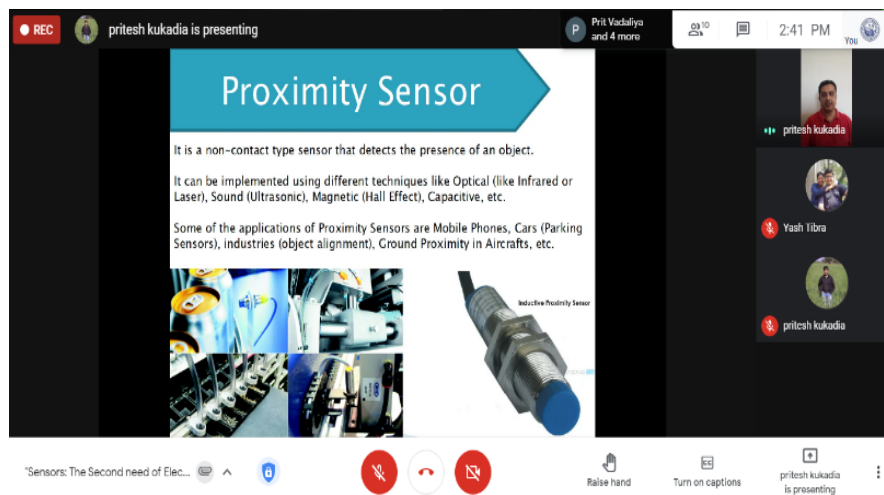


Fig 2: Screen Shot of the Webinar



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**❖ WEBINAR ON HOW TO GET A QUALITATIVE  
INTERNSHIP**

**Event name** : Webinar on how to get a qualitative internship

**Day & Date** : 01<sup>st</sup> May, 2021

**Time** : 10:00 am

**Venue** : Virtual Session on Google Meet (Online)

**Organized by** : Dr. T. D. Pawar, Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 3<sup>rd</sup> Year Students

**Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	Communication Skill
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	01 <sup>st</sup> May, 2021      10:00 am

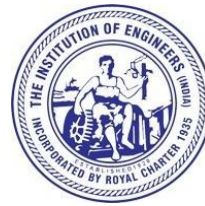


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<b>No of Days for event:</b>	02
<b>Name of speaker:</b>	Mr. Rohan Garg
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	55
<b>Venue name:</b>	Virtual Session on YouTube Live

#### OUTCOMES:

Students get knowledge to construct resumes. They get more ideas about time management, etc.





# Birla Vishvakarma Mahavidyalaya

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### PHOTO GALLERY



Fig 1: Screen Shot of the Webinar

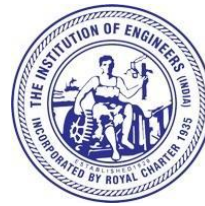


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



#### ❖ Patents and its importance for Engineers and Researchers

**Event name** : Patents and its importance for Engineers and Researchers

**Day & Date** : 24<sup>th</sup> September, 2021

**Time** : 2:00 pm to 4:00 pm

**Venue** : MS Teams (online mode)

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 4th Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	4 <sup>th</sup> year
<b>Specific Subject under which event organized:</b>	Patents Filing
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	24 <sup>th</sup> September, 2021 2 pm to 4 pm



# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	Mr. Harun Shaikh
<b>Department Coordinator Name:</b>	Dr. T. D. Pawar, Dr. J. M. Rathod Prof. Parul H. Panchal
<b>Total No of Students:</b>	157
<b>Venue name:</b>	Virtual Session on MS Teams

#### OUTCOMES:

Students get aware about patents and familiarize themselves with the patent process. Also get knowledge about patent files, license selling of patents and Earn with approved patents. Overall this webinar was very informative and reminiscent. Students get a brief idea of patents and what's going on with the patents in the world.

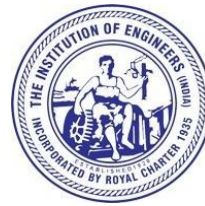


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



#### PHOTO GALLERY:

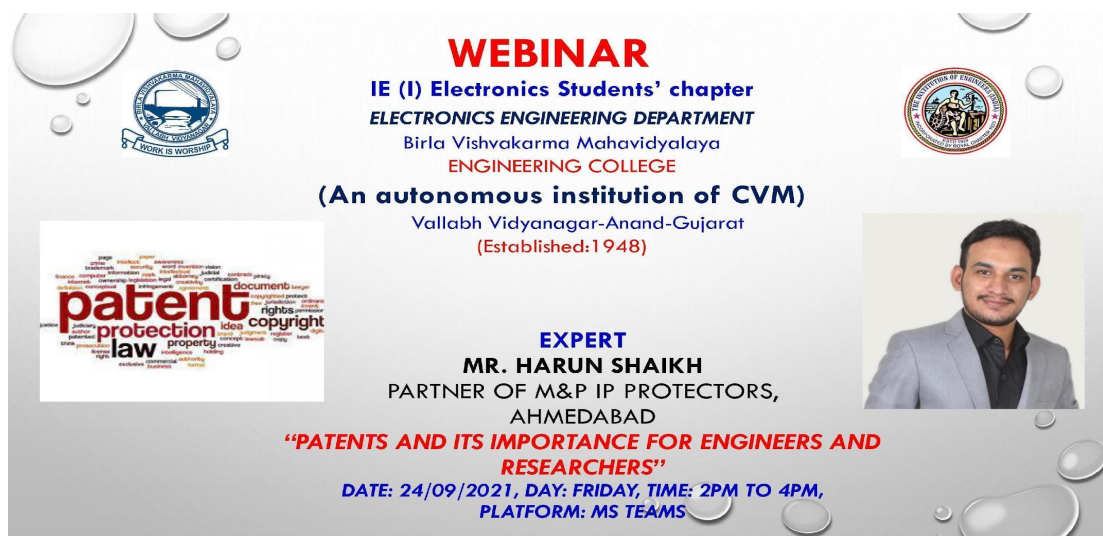


Fig-1: Poster of the Event

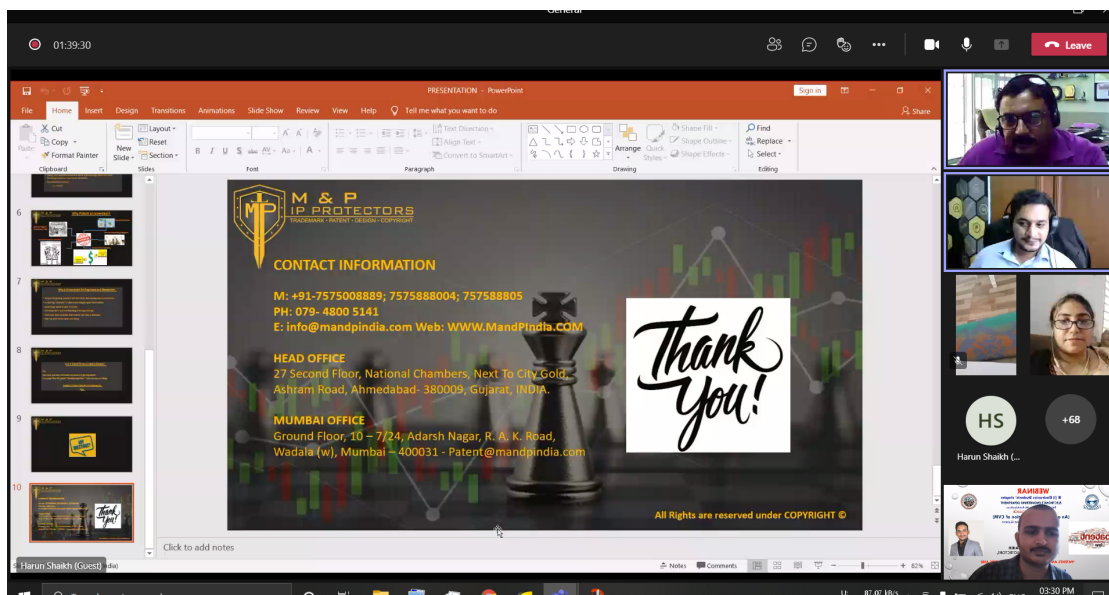


Fig-2: Vote of Thanks



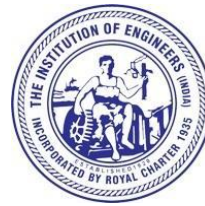


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



#### ❖ Webinar on “Micro to Nano-Electronics”

**Event name** : Webinar on “Micro to Nano-Electronics”

**Day & Date** : 25<sup>th</sup> September, 2021

**Time** : 10:00 am to 12:00 pm

**Venue** : MS Teams (online mode)

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal,  
Prof. M. M. Khambalkar

**Organized for** : All UG Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	VLSI

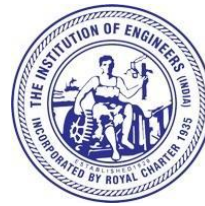


# Birla Vishvakarma Mahavidyalaya

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### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	25th September, 2021 10:00 am
<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	Dr. P. Uma Sathyakam
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod, Prof. P. H. Panchal, Prof. M. M. Khambalkar
<b>Total No of Students:</b>	55
<b>Venue name:</b>	Virtual Session on MS Teams

#### OUTCOMES:

Students got knowledge about the basics of nanotechnology, as well as gained a perspective as to what is nanotechnology and what can be achieved through it, which will be useful to them in future in research topics related to carbon nanotechnology, about CMOS etc.

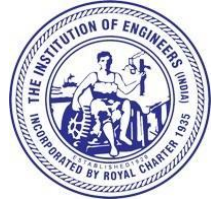


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY



Fig 1: Poster of the Event

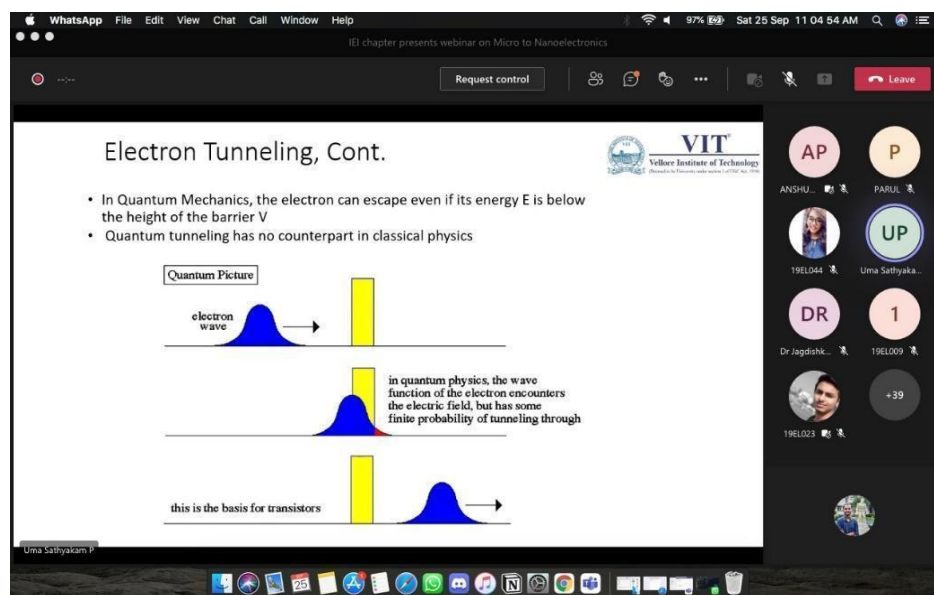


Fig-2: Explaining Electron Tunneling

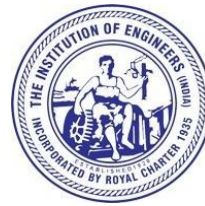


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

**Managed by CVM**

### **Vallabh-Vidyanagar Program Report**



#### **❖ Achieve Your GATE through Dreams**

**Event name** : Achieve Your GATE through Dreams

**Day & Date** : 4<sup>th</sup> September, 2021

**Time** : 11:00 am

**Venue** : Online

**Organized by** : Dr. Kaushika Patel (BVM and Acumen 360)

**Organized for** : 2<sup>nd</sup> & 3<sup>rd</sup> Year Engineering Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	Achieve Your GATE Through Dreams



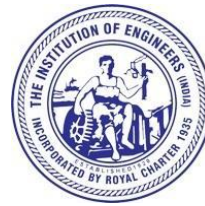


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	4 <sup>th</sup> September 2021 11:00 am
<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	The expert from THE GATE ACADEMY
<b>Department Coordinator Name:</b>	Dr. Kaushika Patel
<b>Total No of Students:</b>	55
<b>Venue name:</b>	Virtual Session

#### OUTCOMES:

Main purpose of this webinar:

- 1) Why Gate is important
- 2) Career opportunities through gate exam
- 3) How gate can help you to get government jobs
- 4) How gate can help you to reach abroad
- 5) How GATE can help you for doing your MBA from best institute off India
- 6) How gate can help you to reach to IISC and IIT

From BVM 65 students participated in this online program

- MBA from best institute of India
- to reach to IISC and IIT

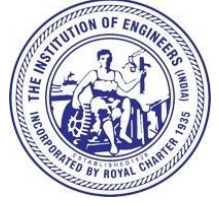


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY

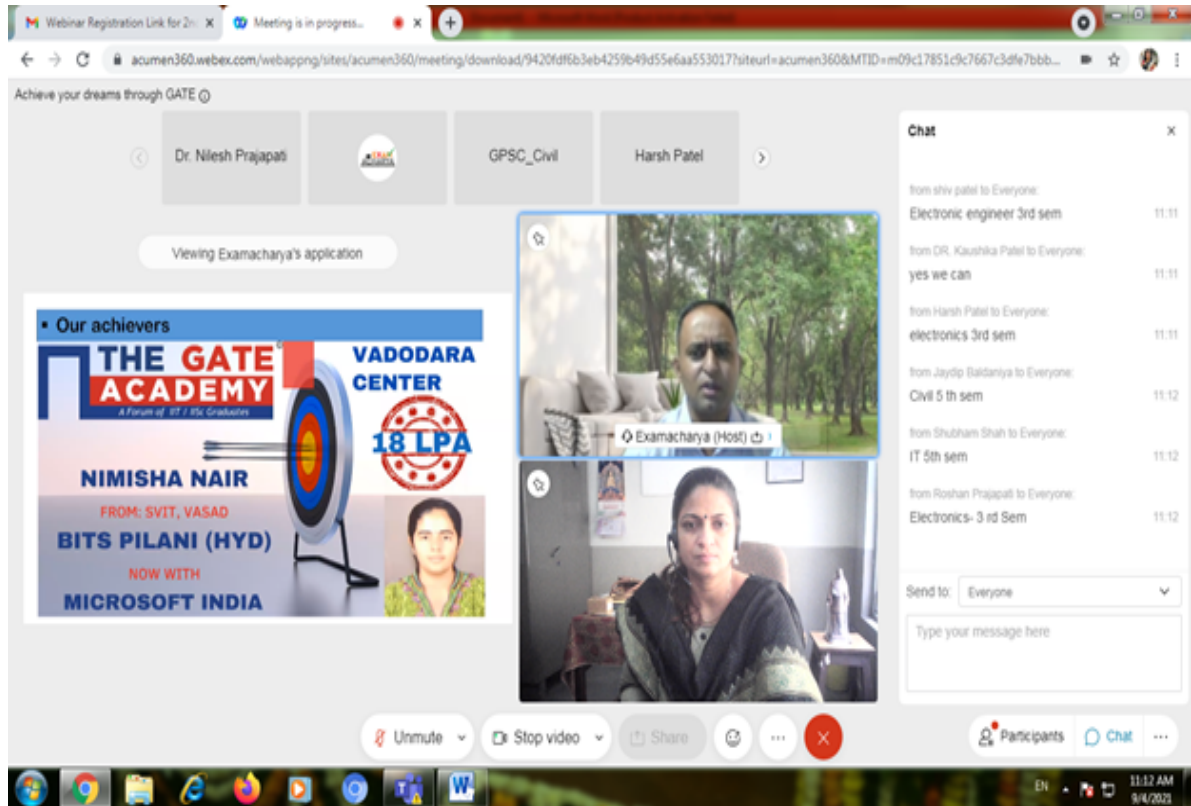


Fig 1: Session for Importance of GATE



**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
Managed by CVM  
**Vallabh-Vidyanagar Program Report**



**❖ 5 Days demo workshops - "Dream Jobs for IT"**

**Event name** : 5 Days demo workshops - "Dream Jobs for IT"

**Day & Date** : 04-10-2021 [Monday] to 08-10-2021 [Friday]

**Time** : 07.00 pm to 08.30 pm

**Venue** : Online

**Organized by** : Dr. Kaushika Patel

**Organized for** : 3<sup>rd</sup>/4<sup>th</sup> Year CS-IT-EE-EC Students

**Program details :**

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	Achieve Your GATE Through Dreams
<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)

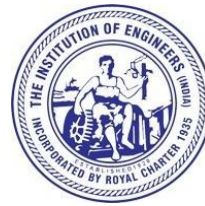


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



<b>Date and Time of event:</b>	4 <sup>th</sup> to 8 <sup>th</sup> October, 2021, 07.00 pm to 08.30 pm
<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	The expert from Acumen360
<b>Department Coordinator Name:</b>	Dr. Kaushika Patel
<b>Total No of Students:</b>	88
<b>Venue name:</b>	Virtual Session

#### OUTCOMES:

Acumen360 with collaboration of BVM Engineering College come up with a focused drive for all Electronics, Electrical, Computer Science and Information Technology Students for preparing them for Coding jobs they offered

##### *5 days Free Workshop Module:*

Day 1: Cognitive Skills for Entrance Exam

Day 2: Quant Training - 1

Day 3: Logical Reasoning - 1

Day 4: Coding Skills

Day 5: Mock Test [Placement Test]

This event will be very much useful for the students opting their career in IT Sectors.

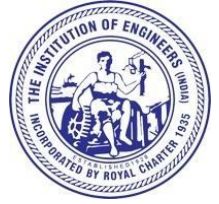




# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM  
Vallabh-Vidyanagar Program Report



### PHOTO GALLERY



Fig 1: Poster of Event

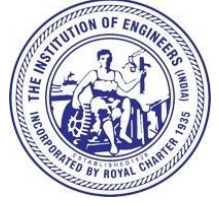


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



#### ❖ Expert Lecture on ASIC Verification

**Event name** : Expert Lecture on ASIC Verification

**Day & Date** : 9<sup>th</sup> October, 2021

**Time** : 1:30 pm to 2:30 am

**Venue** : MS Teams (online mode)

**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal

**Organized for** : 1<sup>st</sup>, 2<sup>nd</sup> and 3<sup>rd</sup> Year B.tech Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year of the students participated:</b>	1 <sup>st</sup> , 2 <sup>nd</sup> & 3 <sup>rd</sup> year
<b>Specific Subject under which event organized:</b>	VLSI

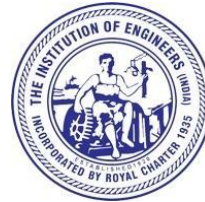


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) Electronics Student's branch (Gujarat Section)
<b>Date and Time of event:</b>	9 <sup>th</sup> October, 2021 1:30 pm to 2:30 pm
<b>No. of days for event:</b>	01
<b>Name of Speakers:</b>	Er. Rutvik
<b>Department Coordinator Name</b>	1. Prof. Chintan S Patel 2. Prof. Chetan J Jayaswal
<b>Total No. of Students</b>	30
<b>Venue Name:</b>	MS Teams

#### OUTCOMES:

- Participants had learned the principle fundamental of VLSI ASIC chip verification practical case study.
- Discussed scope and recent trends in industry of VLSI ASIC chip ASIC verification.
- Discussed student internship and placement.

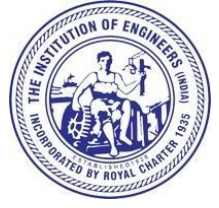


# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY

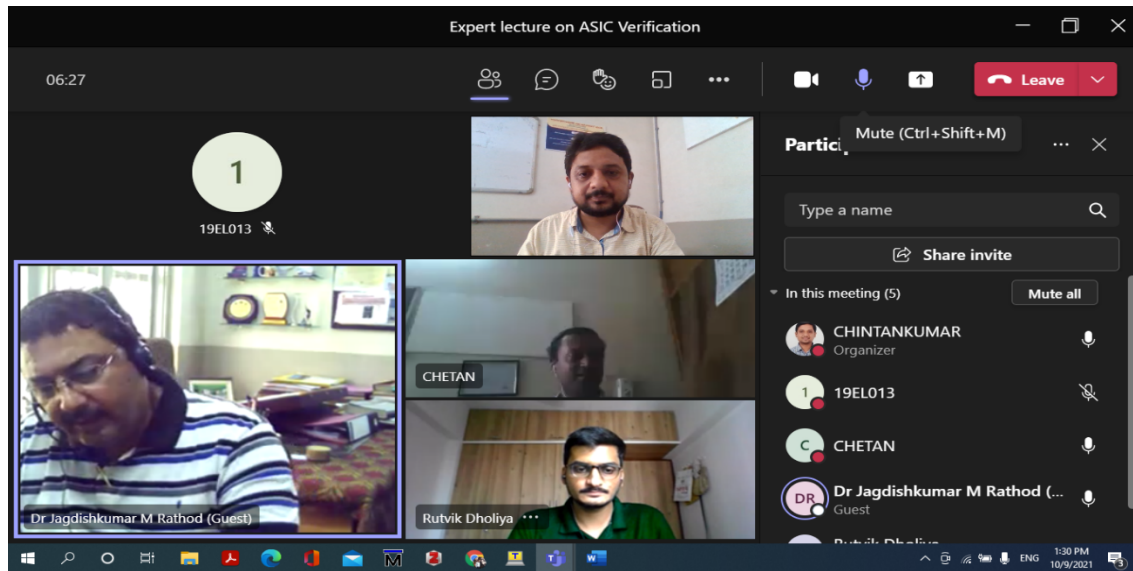


Fig-1: Introduction Section

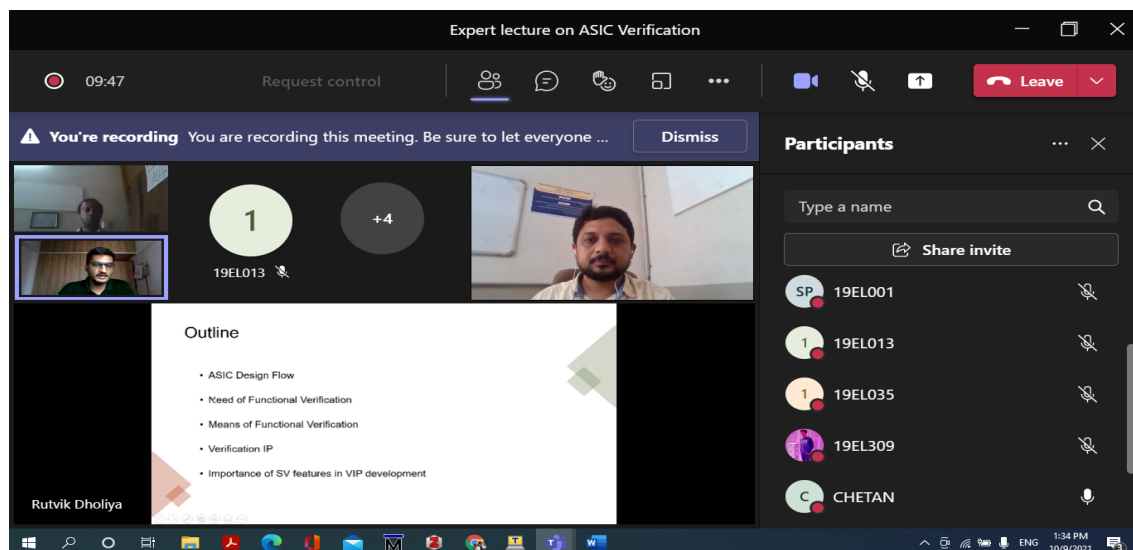
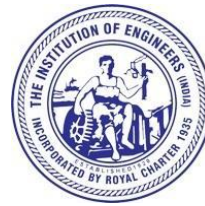


Fig-2: Explaining outlines of the Event





**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
**Managed by CVM**  
**Vallabh-Vidyanagar Program Report**



❖ **Webinar on “MetaMaterial = Matter + Material”**

**Event name** : Webinar on “MetaMaterial = Matter + Material”  
**Day & Date** : 9<sup>th</sup> October, 2021  
**Time** : 11:30 am to 1:30 pm  
**Venue** : MS Teams (online mode)  
**Organized by** : Dr. J. M. Rathod, Prof. P. H. Panchal  
**Organized for** : All UG Students  
**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	MetaMaterial = Matter + Material

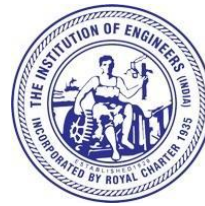


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	9 <sup>th</sup> October, 2021 11:30 am
<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	Dr. Falgun Thakkar
<b>Department Coordinator Name:</b>	Dr. J. M. Rathod, Prof. Parul H. Panchal, Prof. M. M. Khambalkar
<b>Total No of Students:</b>	30
<b>Venue name:</b>	Virtual Session on MS Teams

### OUTCOMES:

Dr. J.M. Rathod described the various student activities undertaken by BVM and student chapters and motivated the students to conduct more such activities. Prof. Mohan khambhalkar introduced the speaker and then the webinar started smoothly.

Then concluded the successful outcome. Students get knowledge about the MetaMaterial, MetaMaterial Construction and Characteristics, Applications of Antennas, Mirage, Application of MetaMaterial etc. Prof. Parul Panchal gave the vote of thanks. In the end, it proved to be very fruitful for the students.



# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



## PHOTO GALLERY

**Birla Vishvakarma Mahavidyalaya**  
Engineering College, V.V. Nagar  
[An Autonomous Institute]

IE(I) Electronics Students' Chapter  
Presents  
webinar on  
**MetaMaterial = Matter + Material**

**Expert: Dr. Falgun Thakkar**

**Specialization Areas:**  
Wavelet based Image Processing, Medical Image Security, Evolutionary Algorithm for AI Systems, RF and Microwave Antenna Designing; Microstrip Patch, DRA and Metamaterial

**Contact for any Query:**  
Kamal Kundariya : 9033901511  
Harsh Chaudhary : 6355787460

**9th October, 2021**  
11:30 A.M. to 1:30 P.M.

IEI Electronics Student Chapter BVM | IEI Electronics | IEI\_ELECTRONICS

Fig 1: Poster of the Event



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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### Vallabh-Vidyanagar Program Report

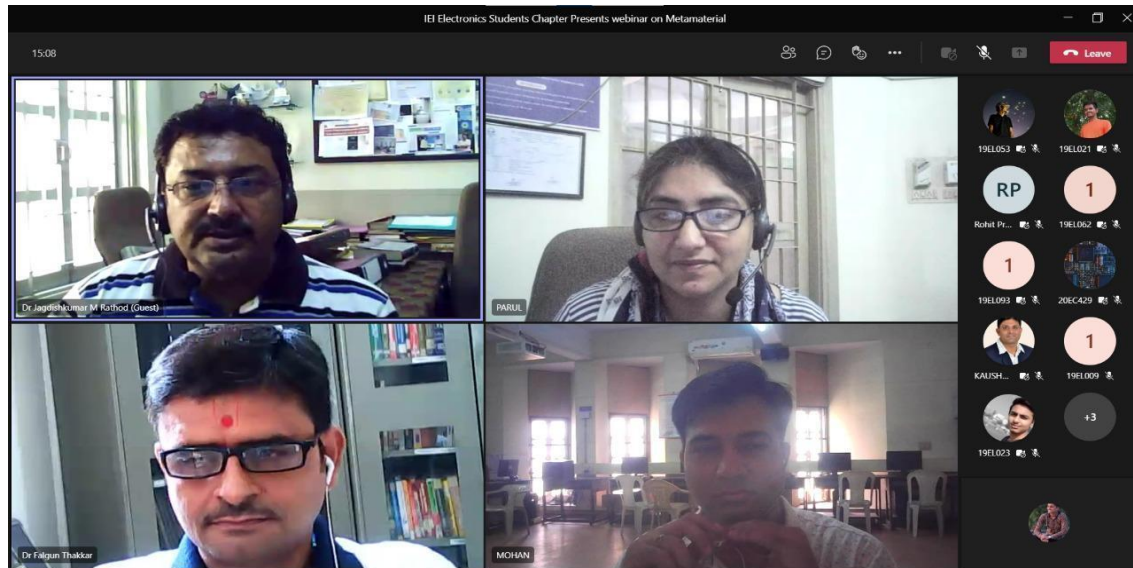
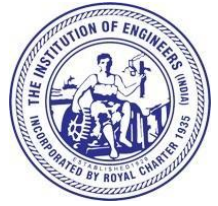


Fig-2: Introduction Section

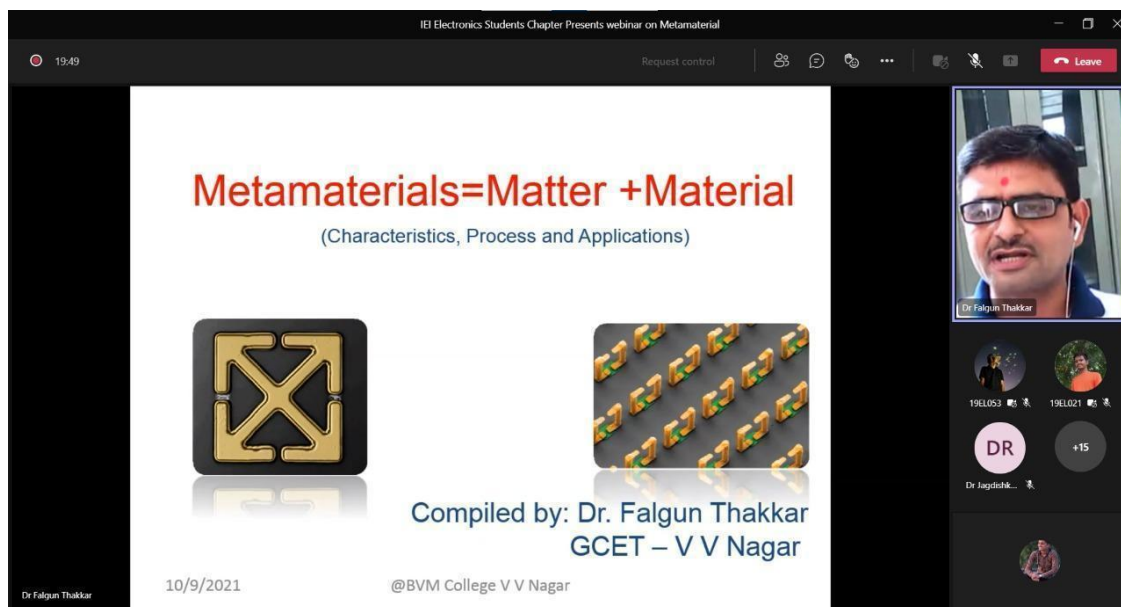


Fig-3: Explaining about Metamaterials



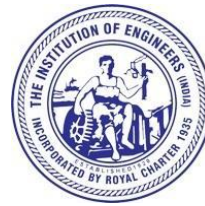


# **Birla Vishvakarma Mahavidyalaya**

## **Engineering College**

**Managed by CVM**

### **Vallabh-Vidyanagar Program Report**



#### **❖ Introduction to STM32 and Logic Analyze**

**Event name** : Webinar on “Introduction to STM32 and Logic Analyzer”

**Day & Date** : 29<sup>th</sup> October, 2021

**Time** : 11:30 am to 12:30 pm

**Venue** : MS Teams (online mode)

**Organized by** : Prof. Anita Bhatt, Prof. C. S. Patel

**Organized for** : All UG Students

**Program details** :

<b>Name of Institute:</b>	BVM Engineering College
<b>Address of Institute:</b>	Vallabh Vidyanagar, Anand
<b>Name of Principal of the Institute:</b>	Dr. I. N. Patel
<b>Department Name of the Institute:</b>	Electronics Department
<b>Name of Head of the Department:</b>	Dr. T. D. Pawar
<b>Year/Semester of the Students participated:</b>	All UG students
<b>Specific Subject under which event organized:</b>	Introduction to STM32 and Logic Analyzer

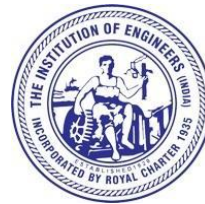


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



<b>Professional body under which event organized:</b>	IE(I) electronics student branch (Gujarat Section)
<b>Date and Time of event:</b>	29 <sup>th</sup> October, 2021 11:30 am
<b>No of Days for event:</b>	01
<b>Name of speaker:</b>	Mr. Kaushal Dekivadia
<b>Department Coordinator Name:</b>	Prof. Anita Bhatt, Prof. C. S. Patel
<b>Total No of Students:</b>	30
<b>Venue name:</b>	Virtual Session on MS Teams

#### OUTCOMES:

Students got knowledge about the basics of STM32, as well as gained a perspective as to what Logic Analyzer is and what can be achieved through it, Which will be useful to them in future in research topics related to Microcontrollers etc.

Prof. Anita Bhatt inaugurated the event by giving the context of the session and the topics to be discussed briefly. She then introduced the speaker of the event which was our own student at BVM, Mr. Kaushal Dekivadiya. This Event ended by vote of thanks by Prof. C.S.Patel.



# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM  
Vallabh-Vidyanagar Program Report



### PHOTO GALLERY

**Birla Vishvakarma Mahavidyalaya**  
Engineering College, V.V. Nagar  
[An Autonomous Institute]

IE(I) Electronics Students' Chapter  
Presents  
webinar on

## Introduction to STM32 and Logic analyser

**Expert : Mr. Kaushal Dekivadia**

**Key points cover in webinar**

- Introduction to blue-pill (STM32 based) development board
- Software development methods for STM32 microcontrollers
- Introduction to software development essentials for Embedded system
- Live demo on blue-pill (stm32f103c8t6), Introduction to logic analyzer

**Coordinators :**  
Prof. Anita Bhatt, Prof. C. S. Patel

**29th October, 2021**  
11:30 A.M. to 1:30 P.M.

**Contact for any Query:**  
Jay Goswami : 6351974779  
Kamal Kundariya : 9033901511

**IEI Electronics Student Chapter BVM** **IEI Electronics** **IEI\_ELECTRONICS**

Fig 1: Poster of the Event



# Birla Vishvakarma Mahavidyalaya

## Engineering College

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Vallabh-Vidyanagar Program Report

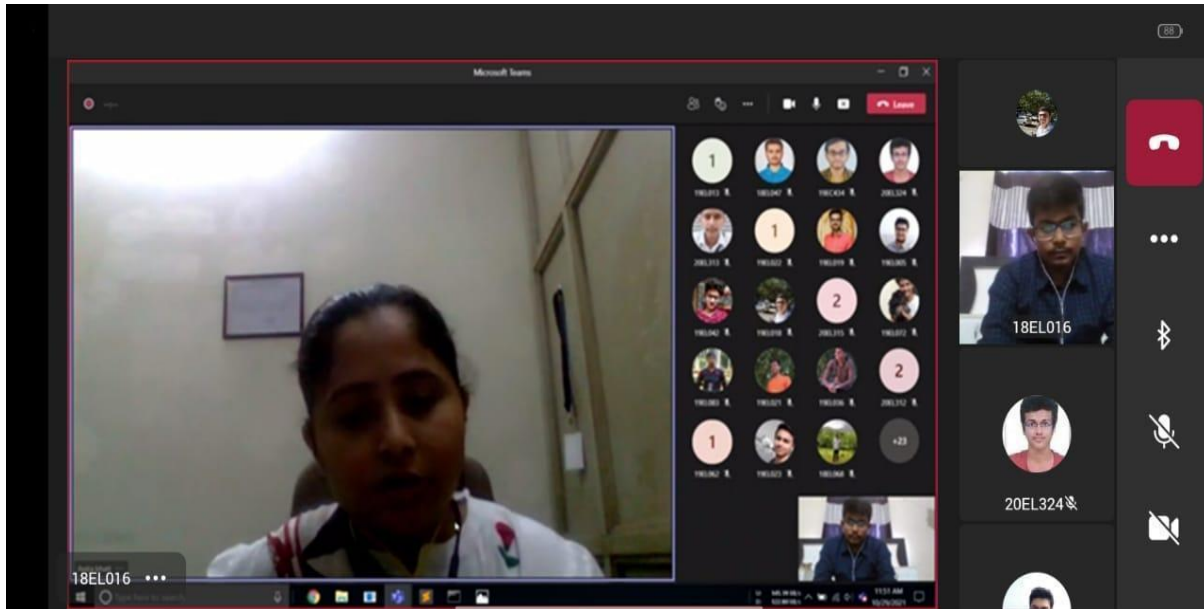
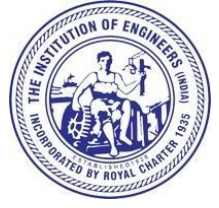


Fig-2: Introduction section

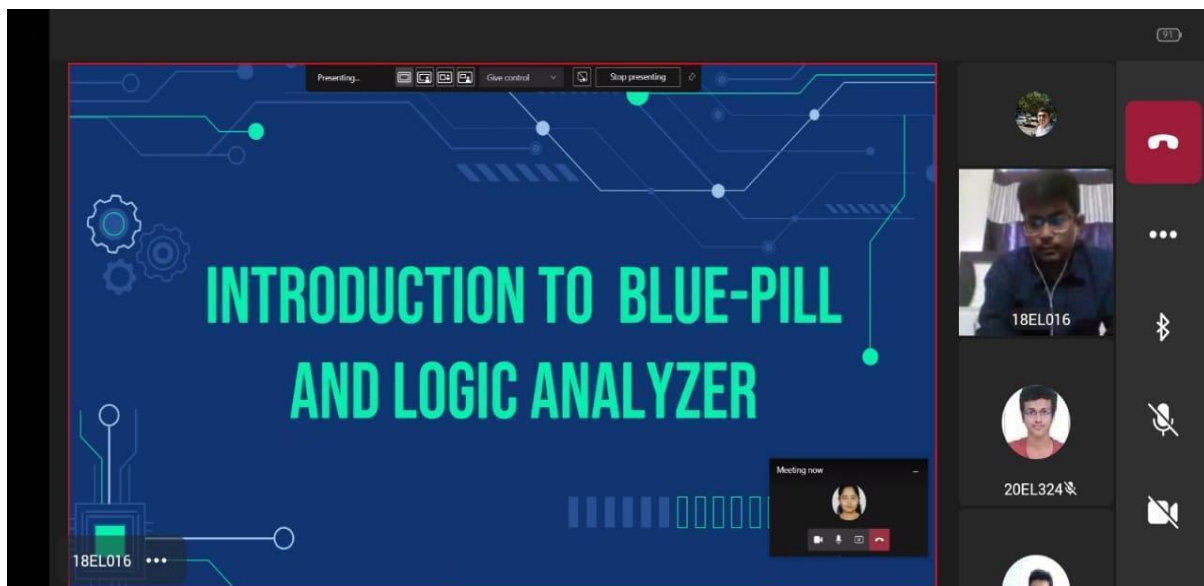
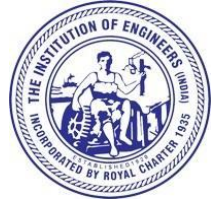


Fig-3: Introduction to Logic Analyzer





**Birla Vishvakarma Mahavidyalaya**  
**Engineering College**  
Managed by CVM  
**Vallabh-Vidyanagar Program Report**



**IE(I) STUDENTS' CHAPTER - ELECTRONICS**  
**CHAPTER CODE - E-388/BVM**  
**B.V.M ENGINEERING COLLEGE,**  
**V.V.NAGAR - 388120**



**Notice -01/BVM/EL**

**Date:12 July, 2018**

**NOTICE**


This is to notify the declaration of the committee of **IE(I) Students' Chapter -Electronics**,  
Chapter code:- **E-388** for academic year **2018-2019**.

**Chairperson: - Arth Shah**


**Secretary: - Mudit Batra**

**Treasurer**

**Kirti Tandel**

  
**Prof P. H. Panchal**  
Faculty Advisor

  
**Dr. J. M. Rathod**  
Secretary

  
**Dr. T. D. Pawar**  
Chairman

**A.Y 2019-20 Year IE(I) Electronics Team**

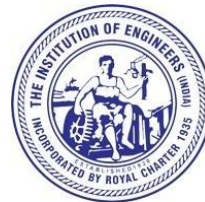


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



IE(I) STUDENTS' CHAPTER – ELECTRONICS

CHAPTER CODE – E-388/BVM

B.V.M ENGINEERING COLLEGE,

V.V. NAGAR - 388120



Notice – 01/BVM/EL

Date: - 26th July, 2019

#### NOTICE

This is to notify regarding the declaration of the committee of IE(I) Students' Chapter – Electronics, Chapter code: - E-388 for academic year 2019-2020.

- Chairperson: - Preeti Kumari
- Secretary: - Isha Vakil

#### Treasurer

1. Parth Makwana (4<sup>th</sup> Year)
2. Dhaval Kambaliya (3<sup>rd</sup> Year)

#### Documentation

1. Shyam Kaneriya (4<sup>th</sup> Year)
2. Yukti Patel (3<sup>rd</sup> Year)

#### Tech. & Tour Team

1. Sanjit Rayat (4<sup>th</sup> Year)
2. Jay (4<sup>th</sup> Year)
3. Vrushal Vara (3<sup>rd</sup> Year)

#### Logistics Team

1. Suman Panchal (4<sup>th</sup> Year)
2. AbhishekSingh Chauhan (3<sup>rd</sup> Year)
3. Fenil Ghoghari (2<sup>nd</sup> Year)


#### Publicity Team

1. Faizan Patel (4<sup>th</sup> Year)
2. Vivek Solanki (3<sup>rd</sup> Year)
3. Dhruvan Dangar (2<sup>nd</sup> Year)

#### Photography Team

1. Meet Modi (4<sup>th</sup> Year)
2. Pranav Mehta (3<sup>rd</sup> Year)

  
Prof. P. H. Panchal  
Faculty Advisor

  
Dr. J. M. Rathod  
Secretary

  
Dr. T. D. Pawar  
Chairman

A.Y 2019-20 Year IE(I) Electronics Team

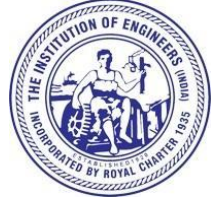


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



IE(I) STUDENTS' CHAPTER - **ELECTRONICS**  
CHAPTER CODE - **E-388/BVM**  
B.V.M ENGINEERING COLLEGE,  
V.V.NAGAR - 388120



Notice - **01/BVM/EL**

Date - 13rd July, 2020

### NOTICE

This is to notify the declaration of the committee of **IE(I) Students' Chapter - Electronics**,  
Chapter code:- **E-388** for academic year 2020-2021.

Chairperson: - Yukti Patel

Secretary: - Vrushal Vara

Treasurer

Vivek Solanki (3rd year)

Documentation Lead

Abhisheksingh Chauhan (3rd year)

Logistics Lead

Viraj Jogani (3rd year)

Tech. & Tour Lead

Dhaval Kambaliya (3rd year)

Publicity Lead

Swati Pandita (3rd year)

Photography Lead

Pranav Mehta (3rd year)

Graphics Team

Rishabh Mishra (3rd year)

Meet Dalwadi (3rd year)

Prof P. H. Panchal  
Faculty Advisor

Dr. J. M. Rathod  
Secretary

Dr. T. D. Pawar  
Chairman

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A.Y 2020-21 Year IE(I) Electronics Team

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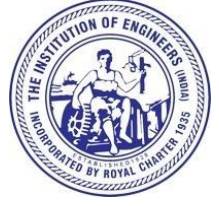


# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



IE(I) STUDENTS' CHAPTER - **ELECTRONICS**  
CHAPTER CODE - **E-388/BVM**  
B.V.M ENGINEERING COLLEGE,  
V.V.NAGAR - 388120



Notice - **01/BVM/EL**

Date - 13rd July, 2021

#### **NOTICE**

This is to notify the declaration of the committee of **IE(I) Students' Chapter - Electronics**,  
Chapter code:- **E-388** for academic year 2021-2022.

Chairperson: - **Harsh Modi**

Secretary: - **Jay Goswami**

**Treasurer**

Keval Bavisi (2nd year)

**Documentation Lead**

Yash Tibra (2nd year)

**Logistics Lead**

Harsh Chaudhary (2nd year)

**Tech. & Tour Lead**

Rahul Sannigrahi (2nd year)

**Publicity Lead**

Dipesh Vadhvani (2nd year)

**Photography Lead**

Hrushikesh Vegad (2nd year)

**Graphics Lead**

Kamal Kundariya (2nd year)

**Anchor-Social Media Handler**

Vaidehi Shah (2nd year)

Prof P. H. Panchal  
Faculty Advisor

Dr. J. M. Rathod  
Secretary

Dr. T. D. Pawar  
Chairman

A.Y 2021-22 Year IE(I) Electronics Team





# Birla Vishvakarma Mahavidyalaya

## Engineering College

Managed by CVM

### Vallabh-Vidyanagar Program Report



BIRLA VISHVAKARMA MAHAVIDYALAYA  
ENGINEERING COLLEGE, VALLABH VIDYANAGAR  
[AN AUTONOMOUS INSTITUTION]

**IE(I) ELECTRONICS STUDENTS' CHAPTER**  
**2021-2022**  
*"Unity is strength"*

 Dr. J. M. Rathod (Secretary)	 Dr. T. D. Pawar (Chairman)	 Prof. P. H. Panchal (Faculty Advisor)		
 Harsh Modi (Chairperson)			 Jay Goswami (Secretary)	
 Keval Bavisi (Treasurer)	 Yash Tibra (Documentation Lead)	 Rahul Sannigrahi (Tech & Tour Lead)	 Kamal Kundariya (Graphics Lead)	
 Dipesh Vadhwani (Publicity Lead)	 Hrushikesh Vegad (Photography Lead)	 Harsh Chaudhary (Logistics Lead)	 Vaidehi Shah (Anchor-Social Media Handler)	

**WHEN THERE IS TEAMWORK AND COLLABORATION, WONDERFUL THINGS CAN BE ACHIEVED**

**Prof. P.H.Panchal**  
Faculty Advisor  
[IE(I) Electronics]

**Dr. T.D.Pawar**  
Head Of The Department

**Dr. J.M.Rathod**  
Secretary  
[IE(I) Electronics]